



**Design Process and Tools Workshop  
Washington DC  
21 June 2005**

- 9:00-9:10**      **Welcome- Lee Pucker**
- 9:10-9:55**      **“Automated Toolset for the Development of SDR Technology”  
Francois Levesque (Communication Research Center, Canada)**
- 9:55-10:40**     **“Streamlining waveform portability across FPGA architectures”  
Andrew Dauman (Synplicity)**
- 10:40-11:00**    **Break**
- 11:00-11:45**    **Embeddable Transceiver Code Generation for SDR Applications by  
Matlab and Simulink"Zoran Kostic (MathWorks)**
- 11:45-12:15**    **DP&T WG Request for Information, Lee Pucker (Spectrum Signal  
Processing)**
- 12:15-1:15**     **Lunch**
- 1:15-2:00**      **“A Case-Study of Xilinx’s System Generator Design Flow for Rapid  
Development of SDR Waveforms” Manuel Uhm, Jim Hwang,  
David Haessig (Xilinx)**
- 2:00-2:45**      **“Issues Associated with SCA Development” Francis Bordeleau  
(Zeligsoft)**
- 2:45-3:30**      **“SDR Domain-specific Language to Simplify Coding” Dominick  
Paniscotti, Steve Jennis (PrismTech)**
- 3:30-3:45**      **Break**
- 3:45-4:30**      **“Develop Reliable Software Defined Radio Systems Faster with  
Model-Driven Development and Optimized Development Tools”  
Dan Mender (Green Hills Software)**
- 4:30-5:15**      **Panel Session**