



SDR Forum
Semiconductor Workshop Agenda
Austin 17 January 2005
(as of 12 January 2005)

0830-0845	Welcome	Jim Gunn
0845-0920	A comparison of competing techniques for wideband channelisation, in the context of a multi-channel software defined radio receiver	Steve Matthews RF Engines
0920-0955	The Application of SDR Concepts in Mobile Devices	Oren Eliezer Texas Instruments
0955-1015	break	
1015-1050	Hardware Acceleration in FPGAs	John Fry Altera
1050-1125	RF MEMS and Tunable Filters	Roger Kuroda XCom Wireless
1125-1200	Novel Digital Transmitter Architecture for Multimode/Multiband Applications	Anthony Dennis M/A-COM
1200-1300	lunch	
1300-1335	Lowering the Power & Cost of SDR Modems: Implementing Shared Resources on an SCA-Enabled System-On-a-Chip	Manuel Uhm Xilinx
1335-1410	Freescale Semiconductor's Reconfigurable Configuration Fabric in Soft Modems	Roman Robles Freescale
1410-1445	Processor architecture and implementation	Rupert Baines picoChip
1445-1520	Low Power DSP	John Glossner Sandbridge
1520-1540	break	
1540-1615	Baseband architecture vision & roadmap for multimode/SDR modem	Alain Chateau Texas Instruments
1615-1650	Processors for Software-Defined Radio: Choices and Trade-offs	Jeff Bier BDTi