

Fixed-Point Aspects of MIMO OFDM Detection on SDR Platforms

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Integrierte Systeme der Signalverarbeitung

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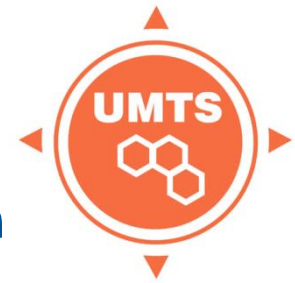




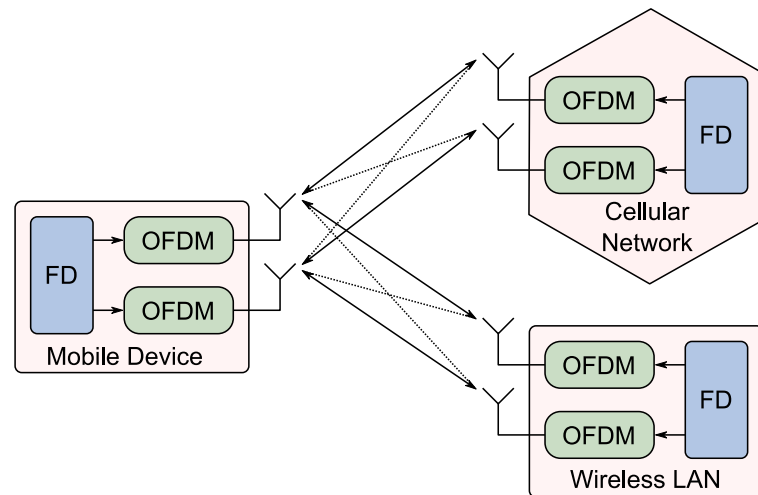
Motivation of Software Defined Radio

- **MIMO OFDM Application**
- **Platform Solutions**
 - Exploiting Data Level Parallelism
 - The P2012 Platform
- **Fixed Point Aspects of MIMO Detection**
 - Problem & Mitigation (QR Decomposition)
 - Algorithmic Performance
 - Execution Time
- **Summary & Outlook**

- **Modern wireless communication**
 - Wireless LANs (stationary)
 - IEEE 802.11 a/b/g/n
 - Cellular networks (mobile)
 - GSM
 - UMTS
 - LTE
 - Cdma2000
- **Merging of stationary & mobile communication**
 - You expect your ...
 - ... smartphone to also support wireless LAN
 - ... laptop to also support cellular networks
- **Need for a flexible, programmable platform**
 - *Software Defined Radio*



- **Characteristics of wireless standards (LTE, 802.11n)**
 - High data rates, low latencies
 - *MIMO*: Multiple antenna transmission
 - *OFDM*: Orthogonal frequency-division multiplexing

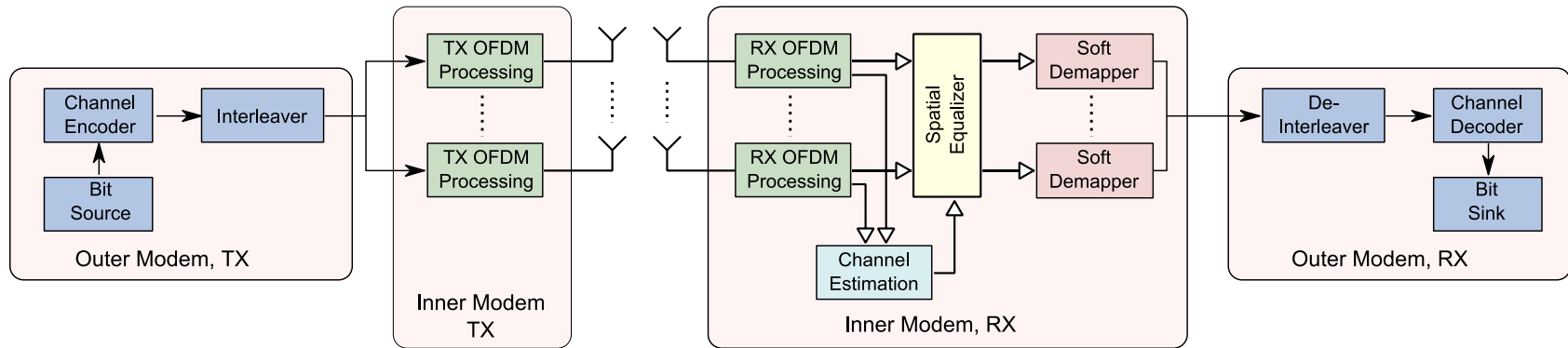


- **SDR platform requirements**
 - *Multi-core*: Handle high throughput, exploit DLP
 - *Common solutions*: SIMD, VLIW
 - *Fast signaling*: Handle low latency

- **Motivation of Software Defined Radio**

➔ **MIMO OFDM Application**

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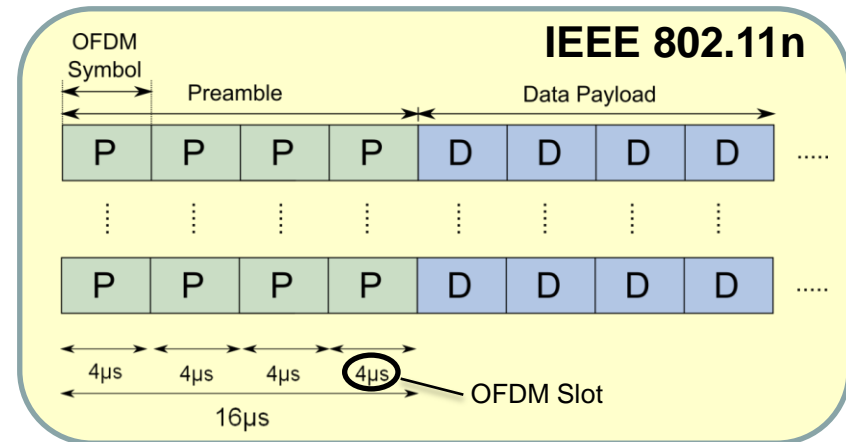


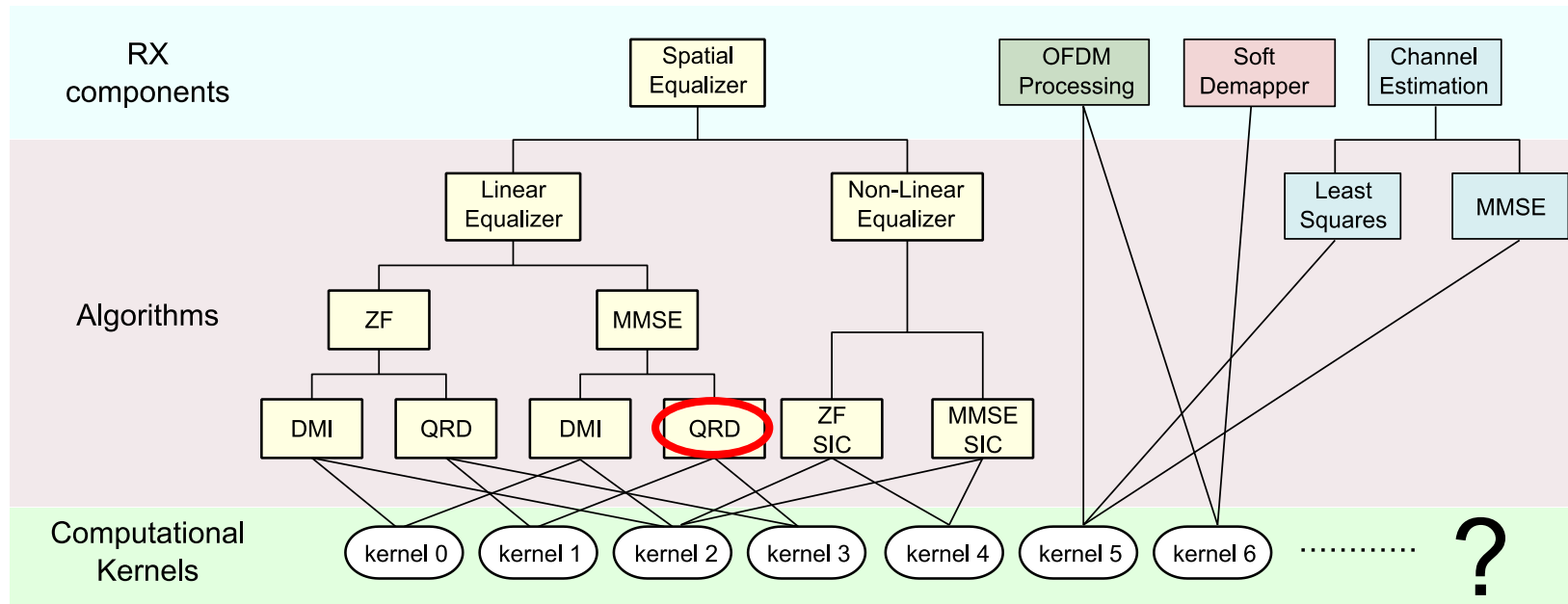
■ Outer Modem

- Channel (De-)coding
- (De-)Interleaving

■ Inner Modem (RX)

- RX OFDM Processing
- Channel Estimation
- Spatial Equalizing: Mitigate channel impact on payload
- Soft Demapping: Calculate soft bits (LLRs)
BPSK, 4QAM, 16QAM, 64QAM





- **Analyze different algorithmic choices within RX blocks**
 - Identify computational kernels
 - Recurring tasks
 - Operate on data with certain alignment
- **Build application as composition of kernels**

- **LMMSE MIMO Equalizer with QRD**

- Basic transmission equation

$$\mathbf{y} = \mathbf{H}\mathbf{x} + \mathbf{n}$$

- Linear MMSE equalization

$$\hat{\mathbf{x}} = \mathbf{G}\mathbf{y}, \quad \mathbf{G} = \left(\hat{\mathbf{H}}^H \hat{\mathbf{H}} + \frac{\sigma_n^2}{E_s} \mathbf{I} \right)^{-1} \hat{\mathbf{H}}^H$$

- Regularized QRD

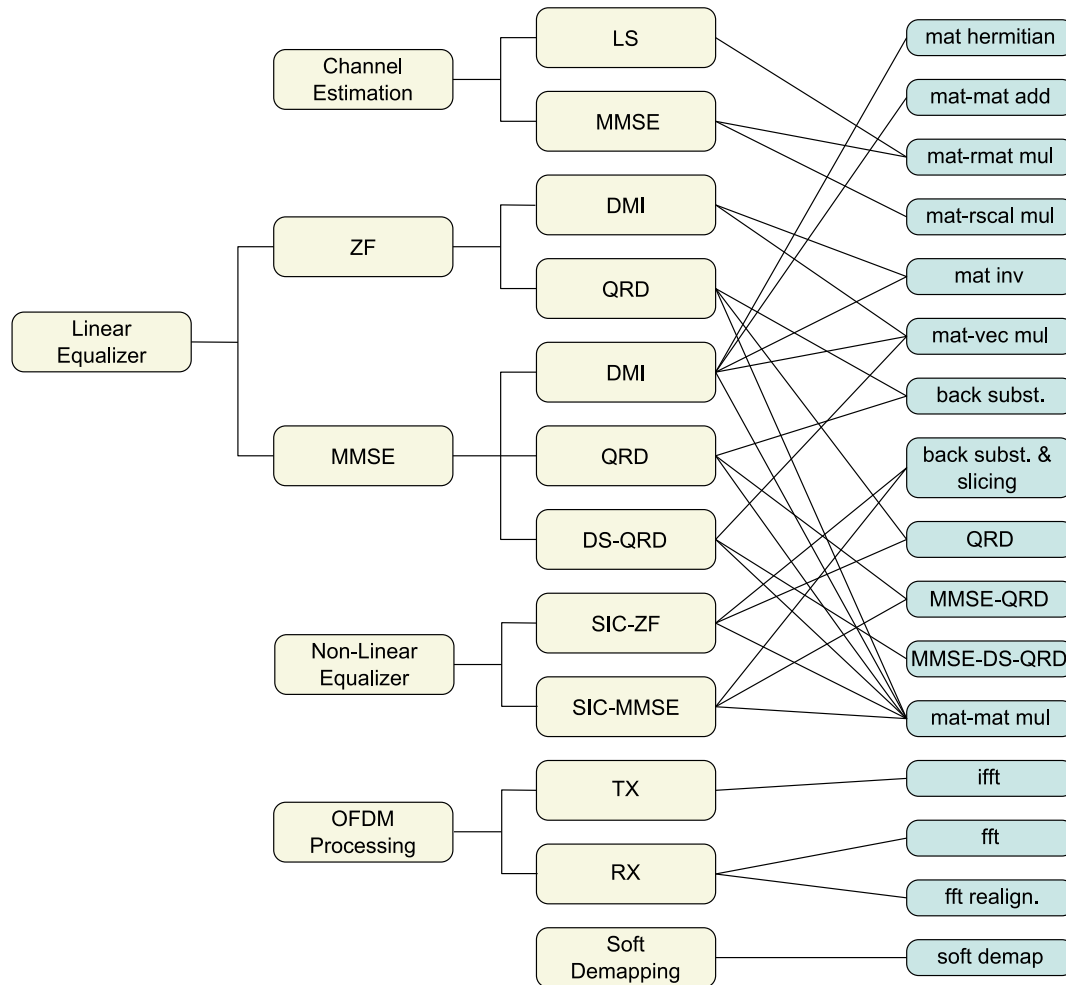
$$\bar{\mathbf{H}} = \begin{pmatrix} \hat{\mathbf{H}} \\ \frac{\sigma_n}{\sqrt{E_s}} \mathbf{I} \end{pmatrix} = \begin{pmatrix} \mathbf{Q}_a \\ \mathbf{Q}_b \end{pmatrix} \mathbf{R}$$

- Rewrite \mathbf{G} using \mathbf{Q}_a and \mathbf{Q}_b

$$\mathbf{G} = \frac{\sqrt{E_s}}{\sigma_n} \mathbf{Q}_b \mathbf{Q}_a^H$$

- **Computational Kernels**

- Regularized QR decomposition
- Matrix-matrix multiplication
- Matrix-vector multiplication



- Application variants consist of a few kernels only
- Kernels implement vector arithmetic
- Suitable platform has to exploit data level parallelism (DLP)

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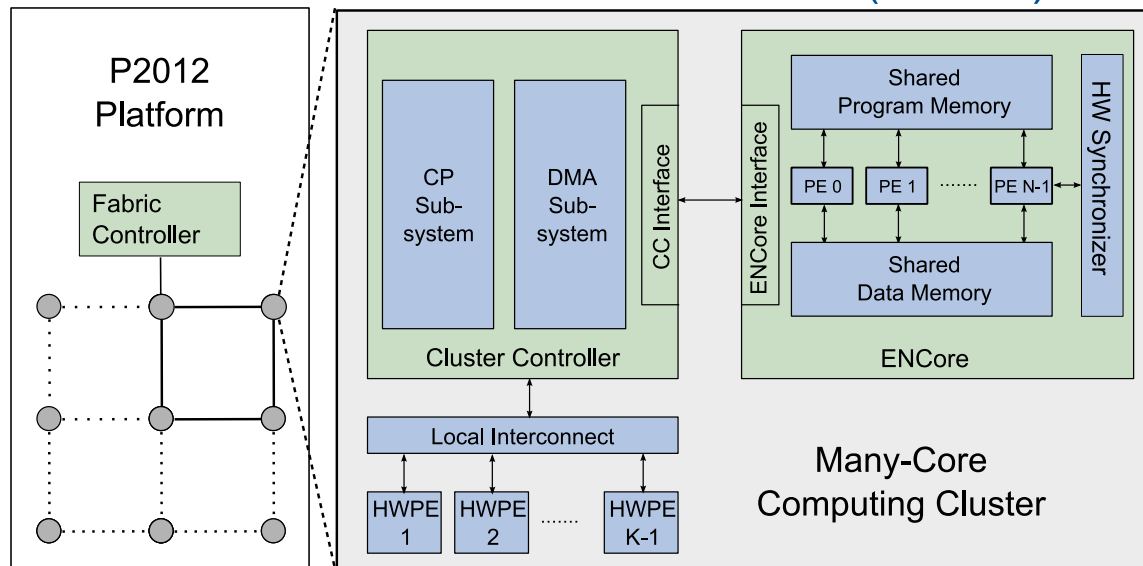
Platform Solutions

- Exploiting Data Level Parallelism
- The P2012 Platform

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- **Two common approaches to exploit DLP**
 - Very Long Instruction Word (VLIW) architectures
 - Instructions are packed into macro instruction and executed in parallel
 - Example: TI TMS320C6000
 - Single Instruction Multiple Data (SIMD) architectures
 - One instruction is executed on a set of data
 - Example
 - ST Ericsson EVP
 - Freescale MSC8156
 - STM P2012
 - Regular data accesses and vectorial kernels call for SIMD architecture

- SoC platform with maximum of 32 clusters
- One cluster provides
 - Max. 16 RISC cores (STxP70) @ 600MHz
 - VECx vector extension (SIMD)
 - 128 bit vector registers
 - **8x16 bit** or 4x32 bit operations
 - Hardware synchronizer for inter-core signaling
 - Interface for hardware accelerators (ASICs)



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Fixed-Point Aspects of MIMO Detection

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-
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■ Problem

- Strict real time constraints of standards imply use of fixed-point operations
- ASIC implementations choose fixed-point bitwidth freely
- DSPs traditionally use 16bit data types
- **Challenge for numerical stability!**

■ Critical point

- Matrix Inversions
- Values run out of fixed point range
- Example: MIMO Preprocessing

$$\mathbf{G} = \arg \min_{\mathbf{G}} E \left\{ |\mathbf{x} - \mathbf{G}\mathbf{y}|^2 \right\} = \left(\hat{\mathbf{H}}^H \hat{\mathbf{H}} + N_0 \mathbf{I} \right)^{-1} \hat{\mathbf{H}}^H$$

- QR Decomposition of augmented channel matrix

$$\bar{\mathbf{H}} = \begin{pmatrix} \hat{\mathbf{H}} \\ N_0 \mathbf{I} \end{pmatrix} = \mathbf{Q}\mathbf{R} = \begin{pmatrix} \mathbf{Q}_a \\ \mathbf{Q}_b \end{pmatrix} \mathbf{R} \quad \mathbf{Q}^H \mathbf{Q} = \mathbf{I} \quad \mathbf{Q}_a \mathbf{R} = \mathbf{H}$$

- Rewriting equalizer matrix

$$\mathbf{G} = N_0 \mathbf{Q}_b \mathbf{Q}_a^H$$

- Choosing Modified Gram-Schmidt (MGS) as QRD algorithm
 - Delivers \mathbf{Q}_b for calculation of \mathbf{G}
 - Project and subtract column vectors for linear independence

```

for  $j = 0$  to  $N_t - 1$  do
  for  $i = 0$  to  $j - 1$  do
     $r_{ji} \leftarrow \mathbf{v}_j^H \mathbf{v}_i$ 
     $\mathbf{v}_j \leftarrow \mathbf{v}_j - \text{proj}_{\mathbf{v}_i}(\mathbf{v}_j) = \mathbf{v}_j - r_{ji} \mathbf{v}_i$ 
  end for
   $r_{jj} \leftarrow \|\mathbf{v}_j\|$ 
   $\mathbf{v}_j \leftarrow \frac{\mathbf{v}_j}{r_{jj}}$ 
end for
 $\mathbf{Q} \leftarrow [\mathbf{v}_0, \mathbf{v}_1, \dots, \mathbf{v}_{N_t-1}]$ 

```

■ Problem

- Repeated projection and subtraction may cause values to run out of fixed point range
- Problem increases with number of spatial streams (4x4)

■ Mitigation: Dynamic Scaling

- One column vector is projected and subtracted from right hand vectors
- Check whether vectors exceed certain range and shift back

Algorithm 1 MMSE MGS-QRD with DS

```

1:  $\mathbf{V} \leftarrow \bar{\mathbf{H}}$ 
2: for  $i = 1$  to  $N_t$  do
3:   for  $j = i$  to  $N_t$  do
4:     if  $\max\{|\Re\{v_{j,1}\}|, |\Im\{v_{j,1}\}|, \dots\} < B_l$  then
5:        $\mathbf{v}_j \leftarrow 2\mathbf{v}_j$ 
6:     else if  $\max\{|\Re\{v_{j,1}\}|, |\Im\{v_{j,1}\}|, \dots\} > B_h$  then
7:        $\mathbf{v}_j \leftarrow \mathbf{v}_j/2$ 
8:     end if
9:   end for
10:   $\mathbf{v}_i \leftarrow \mathbf{v}_i/\|\mathbf{v}_i\|$ 
11:  for  $j = i + 1$  to  $N_t$  do
12:     $\mathbf{v}_j \leftarrow \mathbf{v}_j - (\mathbf{v}_i^H \mathbf{v}_j) \mathbf{v}_i$ 
13:  end for
14: end for
15:  $\mathbf{Q} \leftarrow [\mathbf{v}_1, \mathbf{v}_2, \dots, \mathbf{v}_{N_t}]$ 
    
```

} Dynamic Scaling

■ Problem

- In high SNR region, scaled identity matrix in augmented channel matrix becomes too small to calculate reliable Q_b

$$\bar{\mathbf{H}} = \begin{pmatrix} \hat{\mathbf{H}} \\ N_0 \mathbf{I} \end{pmatrix} = \mathbf{QR} = \begin{pmatrix} \mathbf{Q}_a \\ \mathbf{Q}_b \end{pmatrix} \mathbf{R}$$

■ Mitigation

- Unified Regularized Channel Matrix (URCM)
- Scale up identity matrix

$$\bar{\mathbf{H}}_u = \begin{pmatrix} \hat{\mathbf{H}} \\ \mathbf{I} \end{pmatrix}$$

- Correction factor in projection
- No adaption in subtraction

Algorithm 2 MMSE MGS-QRD with DS and URCM

```

1:  $\mathbf{V} \leftarrow \bar{\mathbf{H}}_u$ 
2: for  $i = 1$  to  $N_t$  do
3:    $\xi_i = (\mathbf{H}^H \mathbf{H})_{i,i} + N_0$ 
4: end for
5: for  $i = 1$  to  $N_t$  do
6:   for  $j = i$  to  $N_t$  do
7:     if  $\max\{|\Re\{v_{j,1}\}|, |\Im\{v_{j,1}\}|, \dots\} < B_l$  then
8:        $v_j \leftarrow 2v_j$ 
9:        $\xi_j \leftarrow 4 \cdot \xi_j$ 
10:    else if  $\max\{|\Re\{v_{j,1}\}|, |\Im\{v_{j,1}\}|, \dots\} > B_h$  then
11:       $v_j \leftarrow v_j/2$ 
12:       $\xi_j \leftarrow 1/4 \cdot \xi_j$ 
13:    end if
14:  end for
15:   $v_i \leftarrow v_i / \sqrt{\xi_i}$ 
16:  for  $j = i + 1$  to  $N_t$  do
17:     $s = (v_i^H \odot a^T) (v_j \odot a)$ 
18:     $v_j \leftarrow v_j - sv_i$ 
19:     $\xi_j \leftarrow \xi_j - |s|^2$ 
20:  end for
21: end for
22:  $\mathbf{Q} \leftarrow [v_1, v_2, \dots, v_{N_t}]$ 

```

- **Status**

- Current algorithm allows 4x4 MIMO LMMSE Detection with algorithmic performance close to floating point

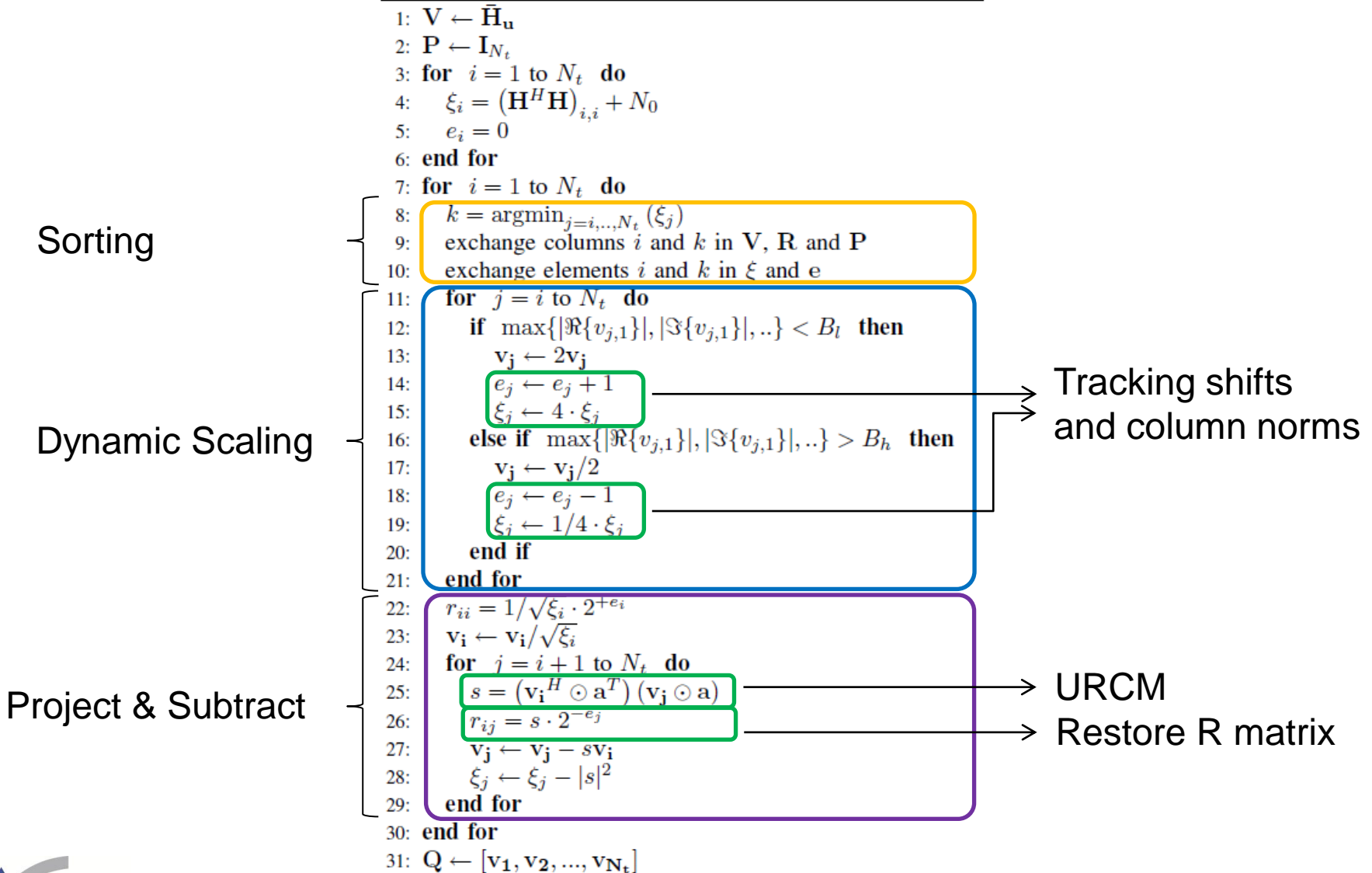
- **Limitation**

- Matrix R is lost due to DS
- No Sorting
- Both expected certain other MIMO detector types
 - MMSE-SIC
 - Sphere Detection

- **Mitigation**

- Keep track of DS shifts to restore R and original column norms

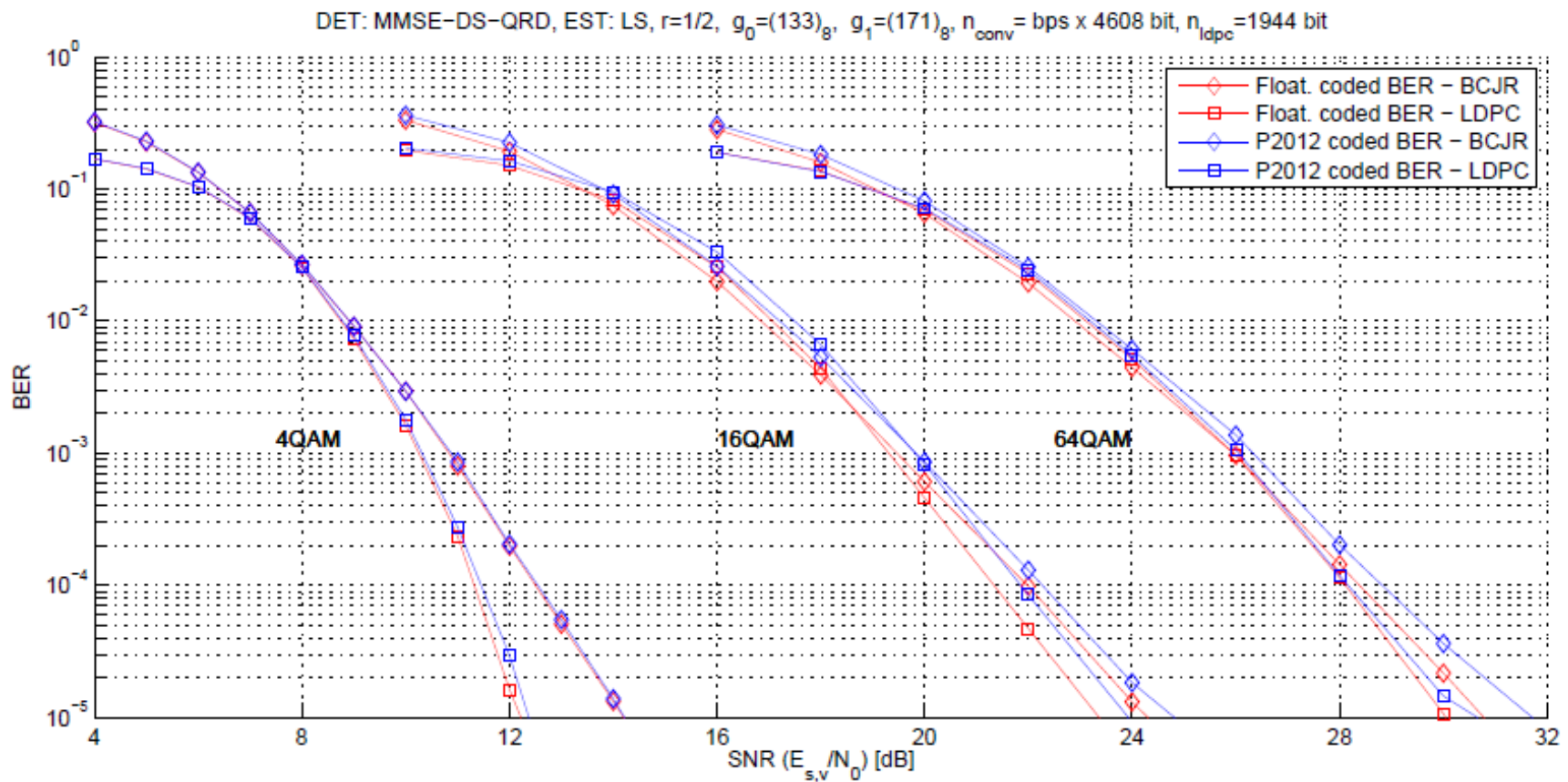
Algorithm 3 MMSE MGS-SQRD with DS and URCM



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Channel Simulation

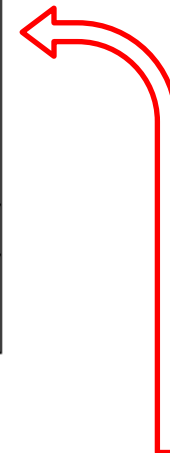
- AWGN
- Rayleigh Fading (20dB drop along 150ns)



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- Algorithmic improvements (DS, URCM) come at the cost of increasing execution time

System	2x2		4x4	
	cycles	T (μs)	cycles	T (μs)
MIMO Preprocessing (per frame)				
mgs-mmse-qrd	22,848	38.08	55,536	92.56
mgs-mmse-ds-qrd (UMCR)	35,424	59.04	66,624	111.04
mgs-mmse-ds-sqrd (UMCR)	43,248	72.08	85,392	142.32
gr-mmse-sqrd	-	-	112,032	186.72
matrix-matrix mul.	2,496	4.16	11,472	19.12
sinr-calc-r	9,456	15.76	25,824	43.04
sinr-calc-r-inv	7,248	12.08	15,600	26.00
Spatial Equalizing (per OFDM slot)				
back substitution	1,188	1.98	2,736	4.56
matrix-vector mul.	1,968	3.28	3,312	5.52



- Note**
 - QRD algorithms with lower operation count (Givens Rotation) are not faster on SIMD platform
 - Reason: Irregular data accesses

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Summary & Outlook

■ Summary

- Numerical stability is a critical point in MIMO detection
- MIMO detection can reach close to floating point algorithmic performance on 16bit fixed point DSPs
- Moderate additional costs in execution time

■ Outlook

- VLIW architectures
- Advanced, iterative receivers
- Customized ASIP for baseband processing

Thank you!

