

Software Radio Spectrum Analyzer

Jérôme PARISOT, Emilian LE SUR,
Christophe MOY, Daniel LE GUENNEC, Pierre LERAY

SUPELEC/IETR

27 June 2012

SUPELEC - Campus de Rennes

SCEE – Signal, Communications et Electronique Embarquée

IETR – UMR CNRS 6164

Institut d'Electronique et Télécommunications de Rennes

- **Student project**
 - implement real radio on a part-time 3 months project
 - evaluate/dimension SDR capabilities for real-time processing
 - not only for communications
- **System**
 - SDR approach
 - USRP N210 from Ettus research
 - Simulink processing environment



- Power spectral density
- Simulink implementation on N210 platforms
- Windows implementation on N210 platforms



Conclusion

- Power spectral density
- Simulink implementation on N210 platforms
- Windows implementation on N210 platforms

- **Spectrum**

- continuous

$$X(f) = \int_{-\infty}^{+\infty} x(t) \cdot e^{-2\pi f t} dt$$

- discrete

$$\hat{X}(f) = T \sum_{k=1}^N x(k) \cdot e^{-2\pi f k T}$$

- but : amplitude and phase
- convergence not guaranteed mathematically

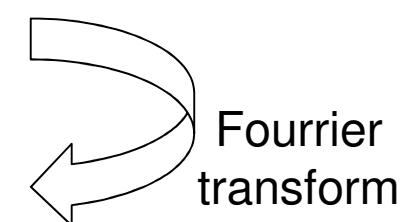
- **Power Spectral Density**

- auto-correlation

for real signals

$$\gamma_x(\tau) = E\{x(t + \tau) \cdot x(t)\}$$

$$\Gamma_x(f) = \int_{-\infty}^{+\infty} \gamma_x(\tau) \cdot e^{-2\pi f \tau} d\tau$$



Fourrier
transform

- **Power Spectral Density**

- it can be shown that

→ periodogram

→ instead: (Schuster - 1898)

$$\Gamma_x(f) = \lim E \left[\frac{|\hat{X}(f)|^2}{N.T} \right]$$

$$\hat{\Gamma}_x(f) = \frac{|\hat{X}(f)|^2}{N.T}$$

- but: it can be shown that estimation error standard deviation (hypothesis of White Gaussian Noise)

$$\sigma_{\hat{\Gamma}_x(f_0)} \approx \Gamma_x(f_0)$$

- error is at the level of the measure!

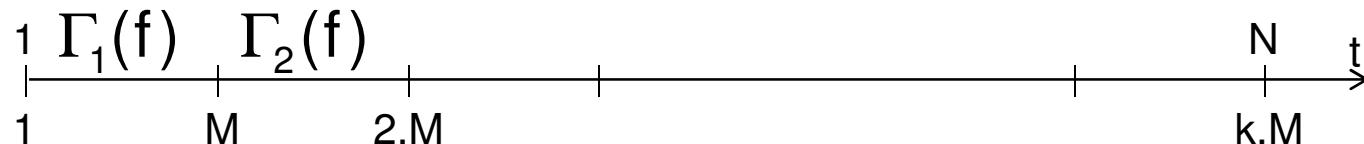
- necessary to average

- Welch approach



- Discrete PSD by Welch method

- based on temporal samples (periodogram-based)
- subdivide the samples in temporal slots
- combine the PSD result of each slot in order to make a global mean PSD



- advantage on precision

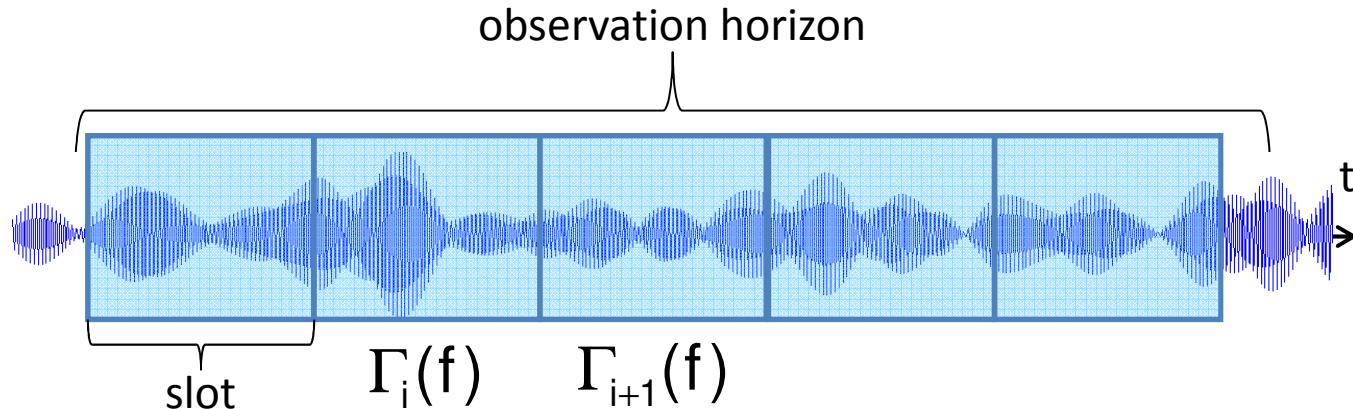
$$\sigma_{\hat{\Gamma}_x(f_0)} = \frac{1}{\sqrt{k}} \sigma_{\Gamma_i(f_0)}$$

$$\hat{\Gamma}_x(f) = \frac{|\hat{X}(f)|^2}{N.T}$$

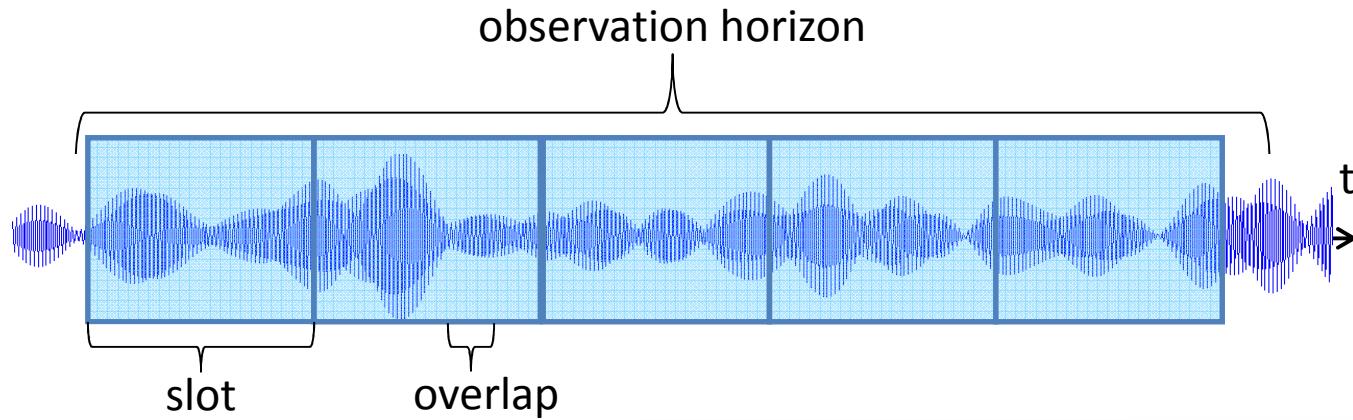
- disadvantage

- resolution

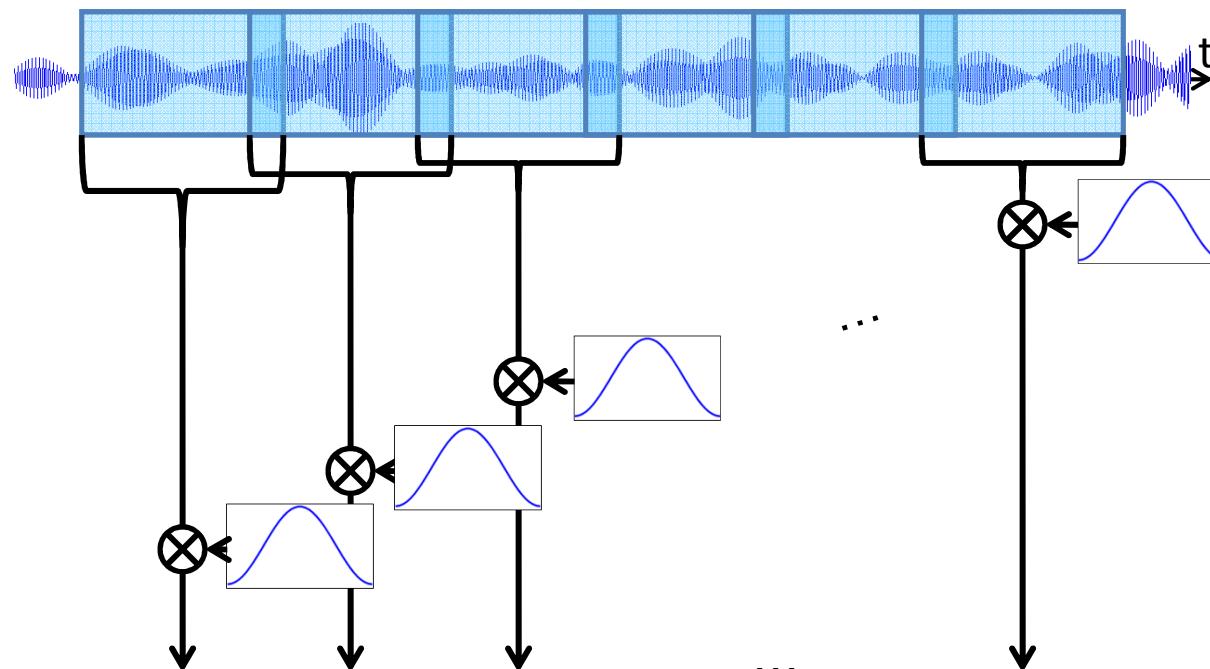
$$\hat{\Gamma}_x(f) = \frac{1}{k} \sum_{i=1}^k \hat{\Gamma}_i(f)$$

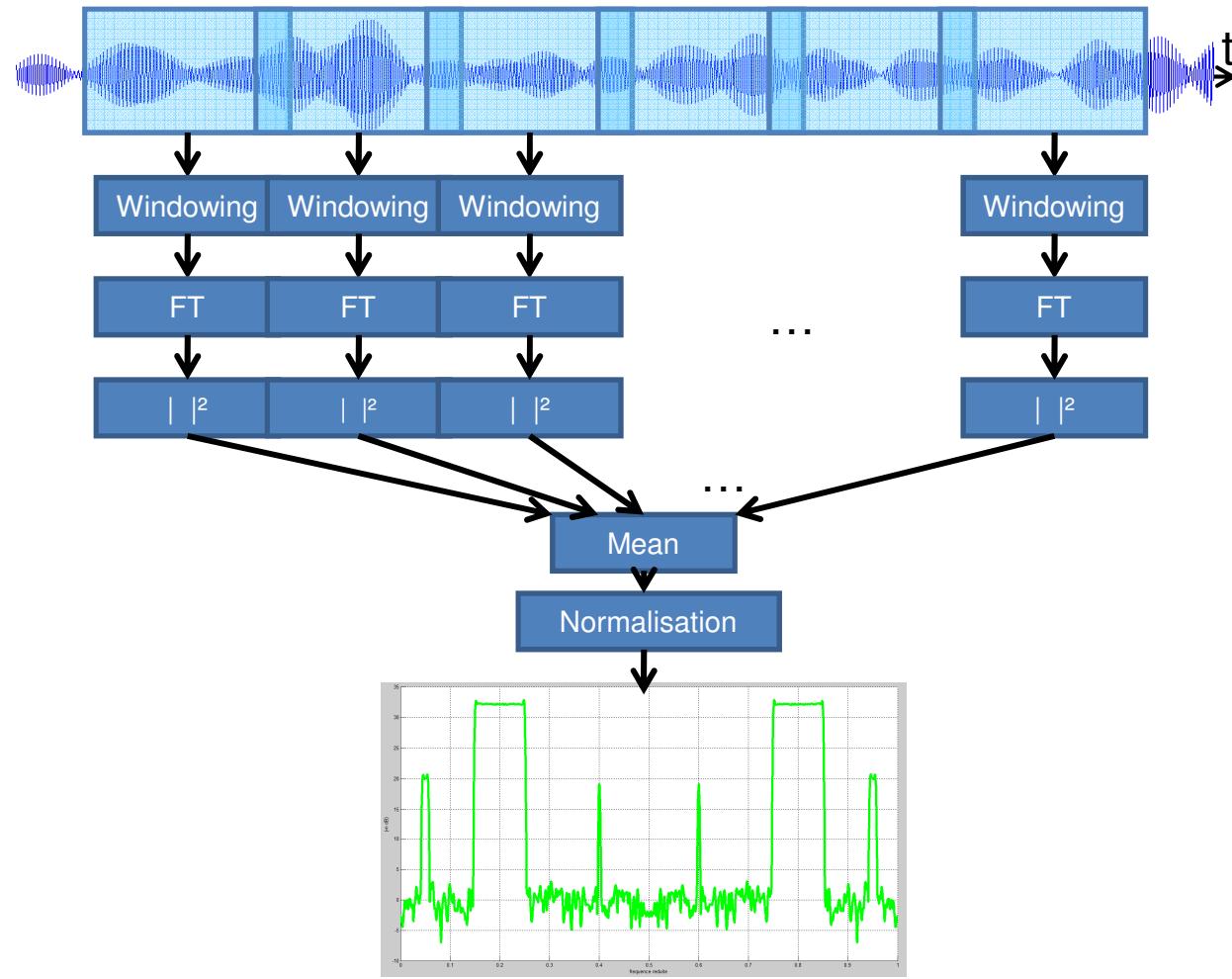


- **Overlap**
 - avoid loosing effects at the slot border



- **Windowing**
 - in order to mitigate truncature effect
 - Hamming here





- **Function for Welch algorithm**

- input: time samples
- output: PSD
- parameters:
 - number of slots
 - overlapping ratio
 - windowing type

```
signalOut = zeros(1,sizeFFT);  
  
for i=0:nbSlots-1  
  
    offset=doffsetSlot*i+1;  
    % Slot extraction and windowing.  
    slot= signalln(offset:offset + slotSize-1).*fen;  
    % Normalize FFT.  
    S = fft(slot, sizeFFT)/slotSize;  
    % Square  
    signalOut = signalOut + abs(S).^2;  
  
end  
  
% Mean and normalization.  
signalOut = signalOut/ nbSlots * slotSize/ norm_win;
```

```
function [ signalOut ] = algoWelch( signalln , nbSlots , overlapRatio, window)
```

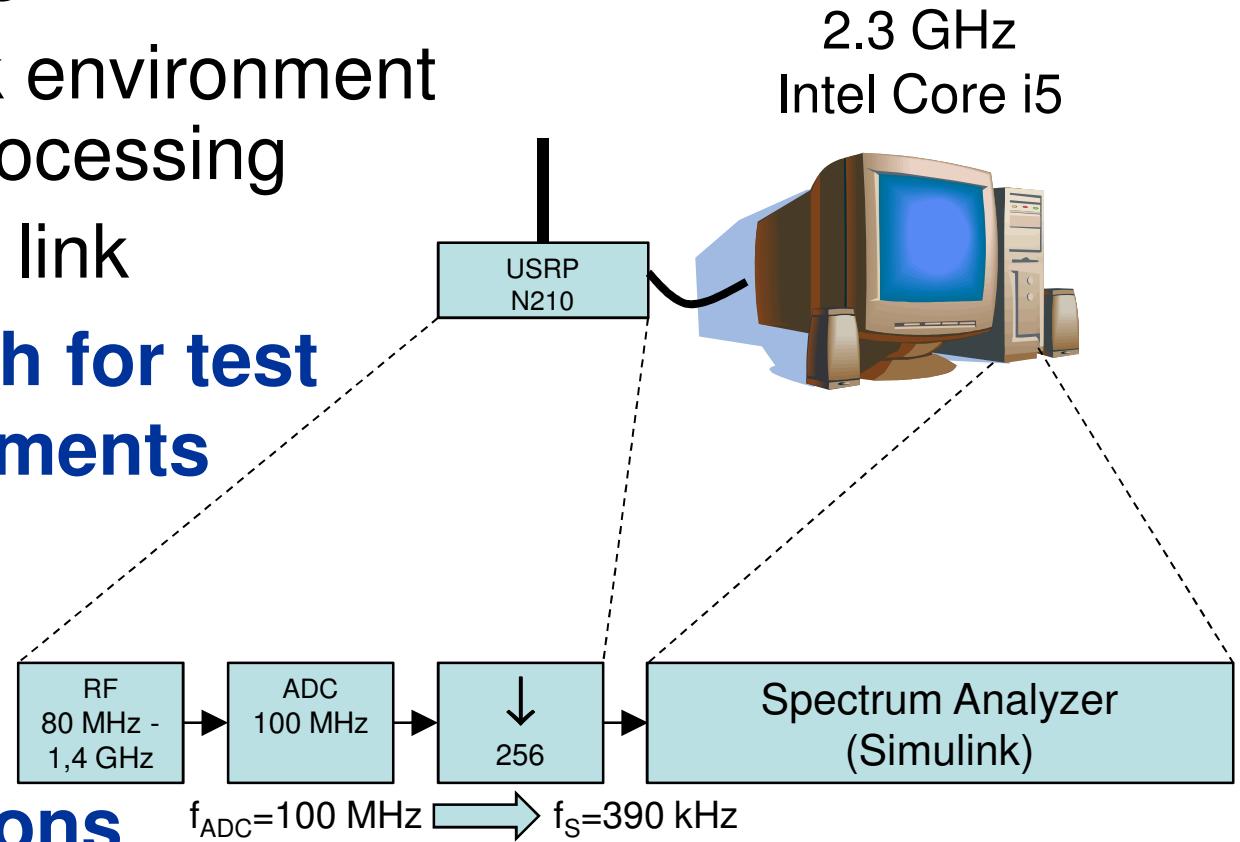
- Power spectral density
- **Simulink implementation on N210 platforms**
- Windows implementation on N210 platforms

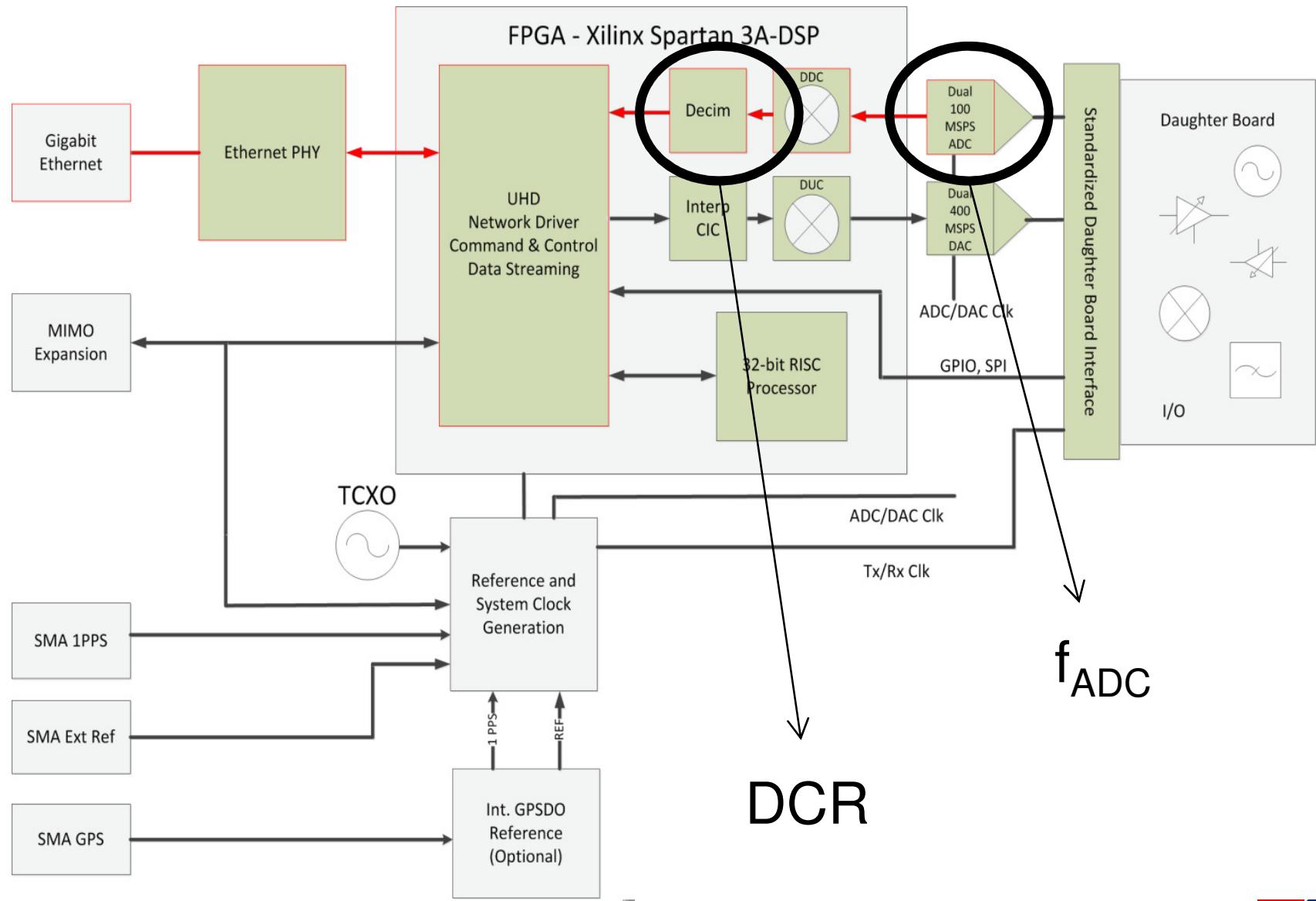


Conclusion

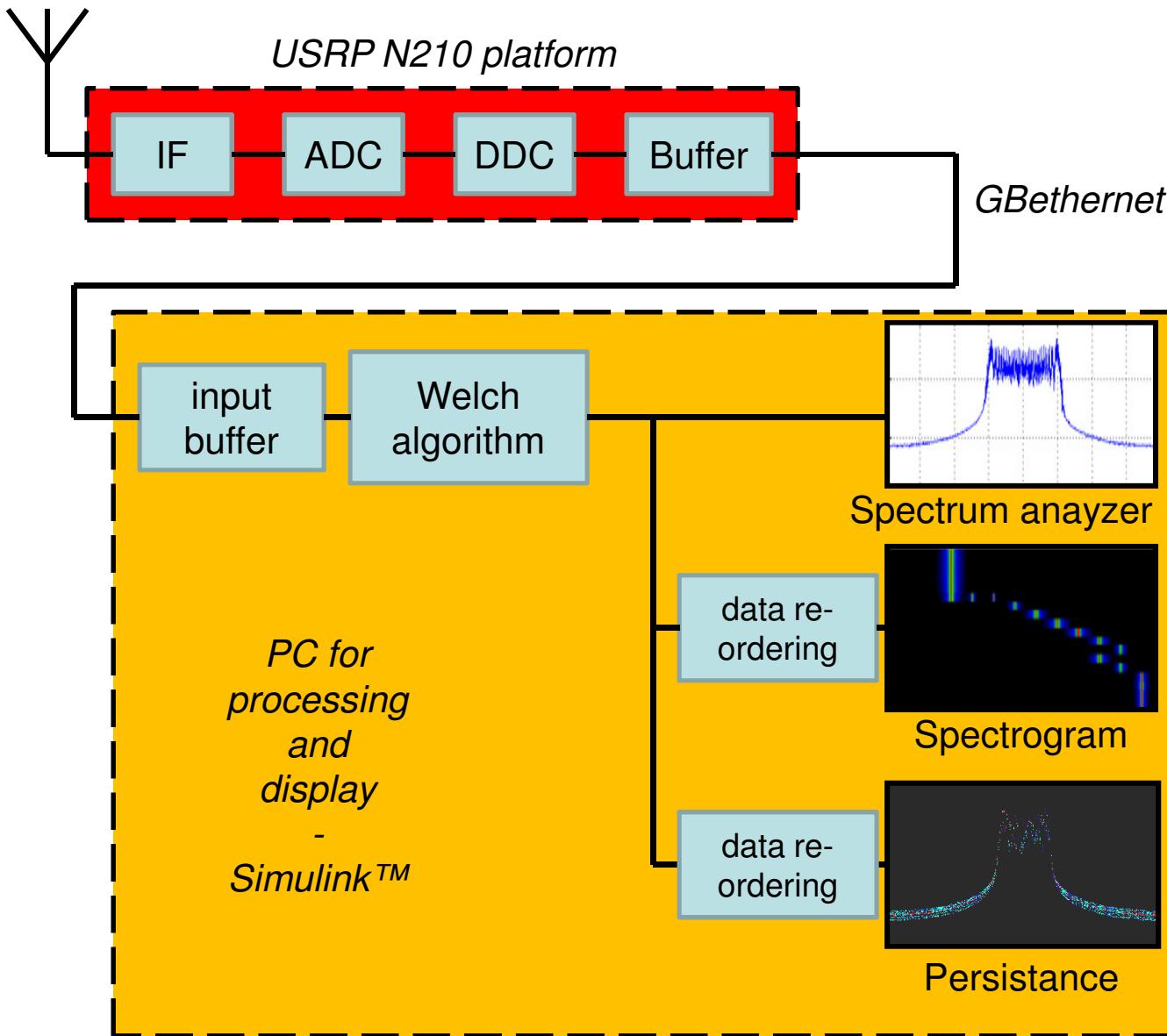


- **USRP based platform (N210 from Ettus™)**
 - UHD drivers
 - for Simulink environment
 - real-time processing
 - GBethernet link
- **SDR approach for test and measurements**
- **Not only for communications**

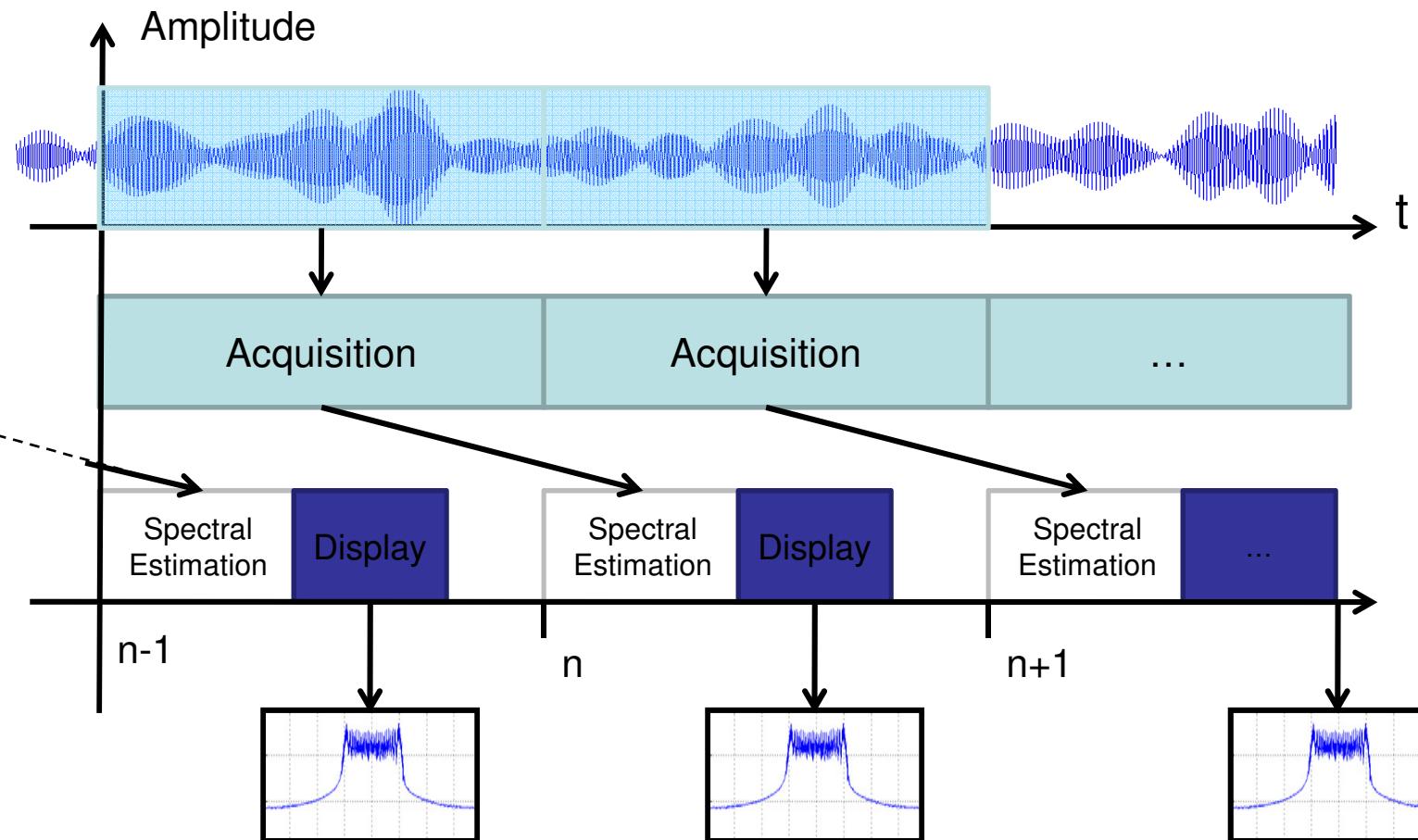




System functional view



- Spectrum analyzer



- **Spectrum analyzer performance**

- bandwidth

- DCR: undersampling factor

- display frequency (of PSD)

- n_f : number of samples per frame
- n_b : number of frames

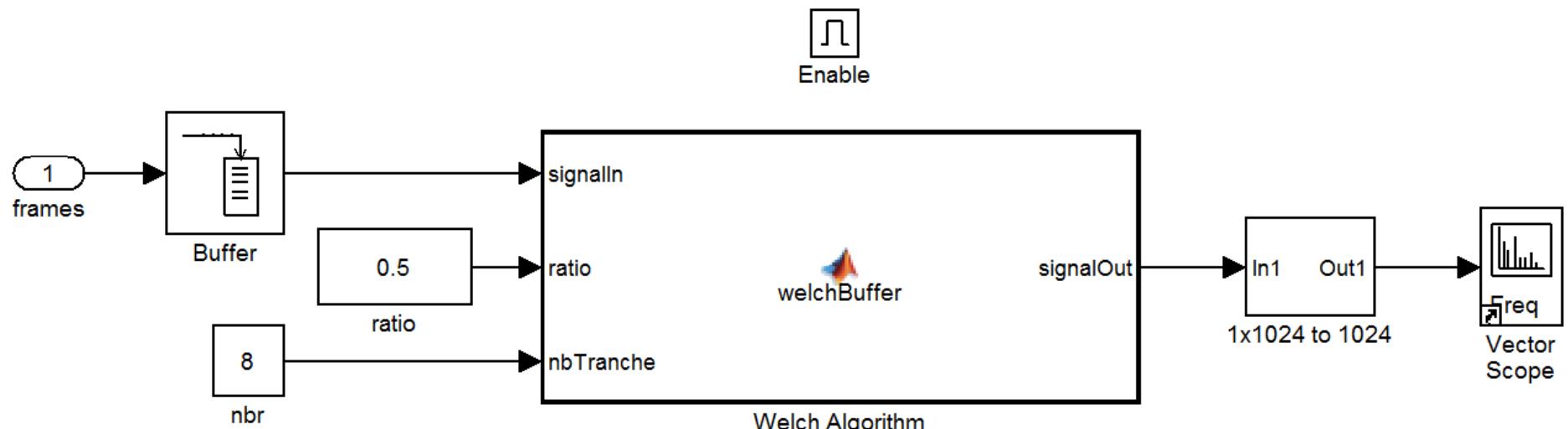
$$f_{\text{Max}} = \frac{f_{\text{ADC}}}{2 \cdot \text{DCR}}$$

$$f_{\text{disp}} = \frac{f_{\text{ADC}}}{\text{DCR}} \cdot \frac{1}{n_f \cdot n_b}$$

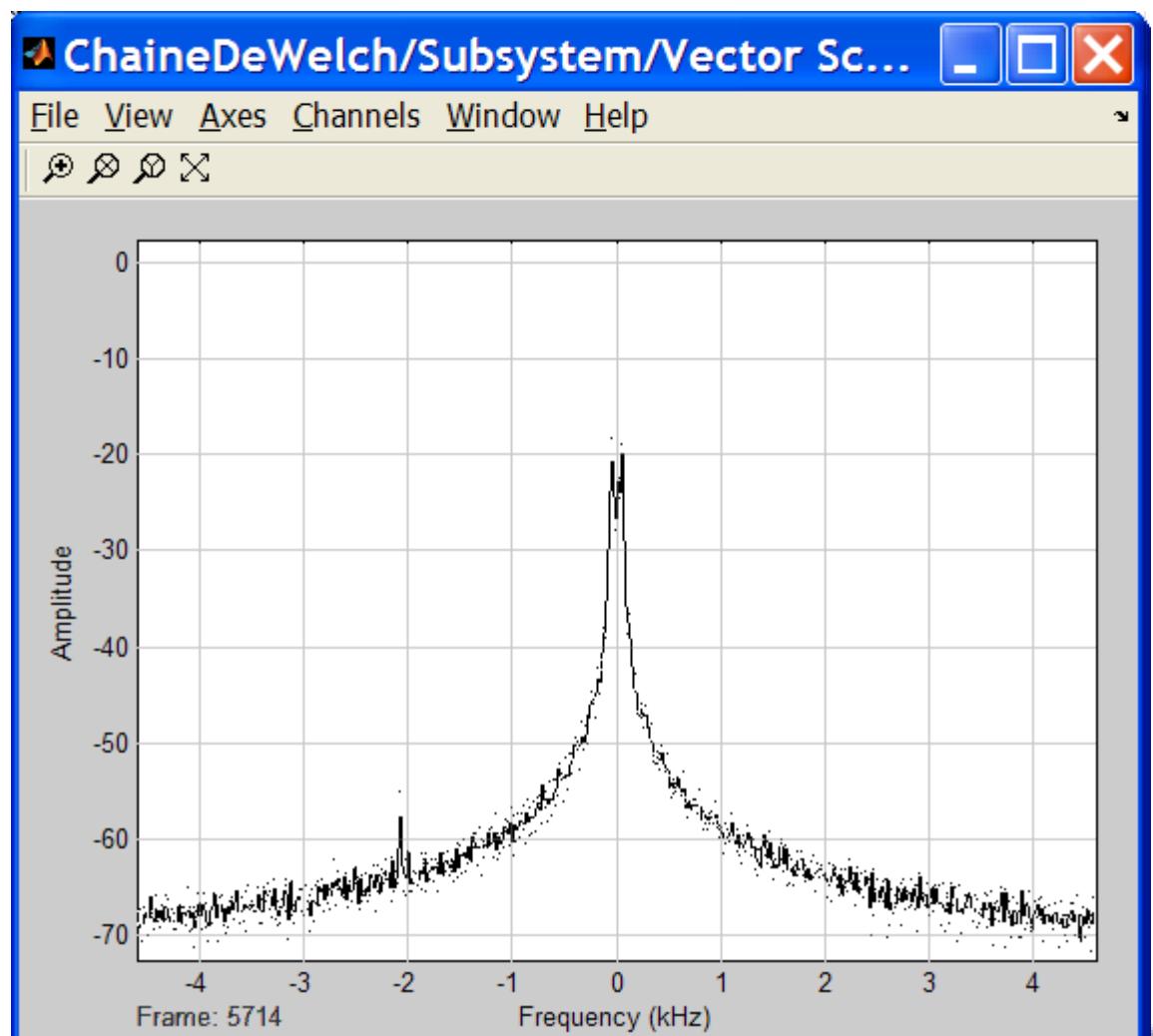
- real time (2.3 GHz Intel Core i5)

Fc (center freq.)	Decim	bandwidth	Frame Length	Sample Time (s)	Output data type
80 Mhz – 1.4 GHz	256	195 kHz	362	$2,56 \cdot 10^{-6}$	double

- Spectrum analyzer in Simulink

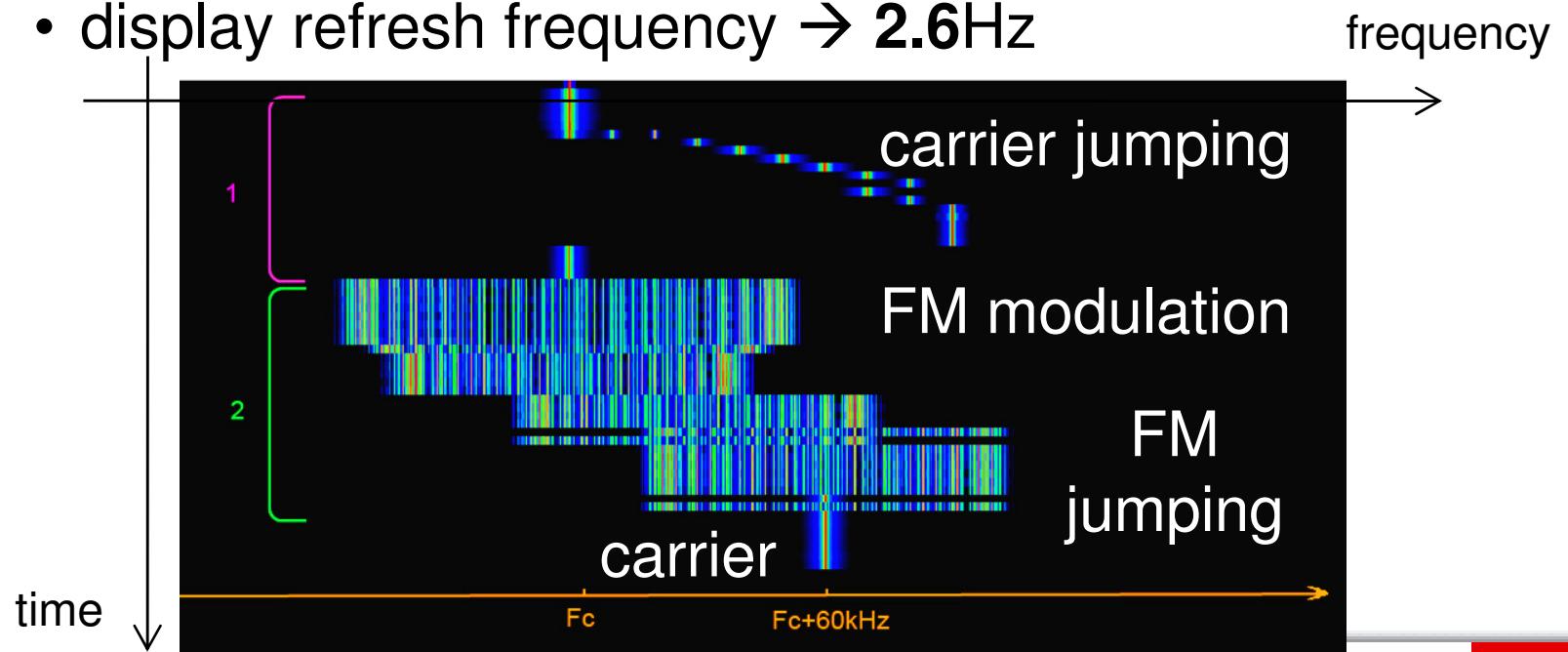


- **Spectrum analyzer**
 - FM 10 kHz signal
 - DCR = 256
 - DCR = 128
 - DCR = 64



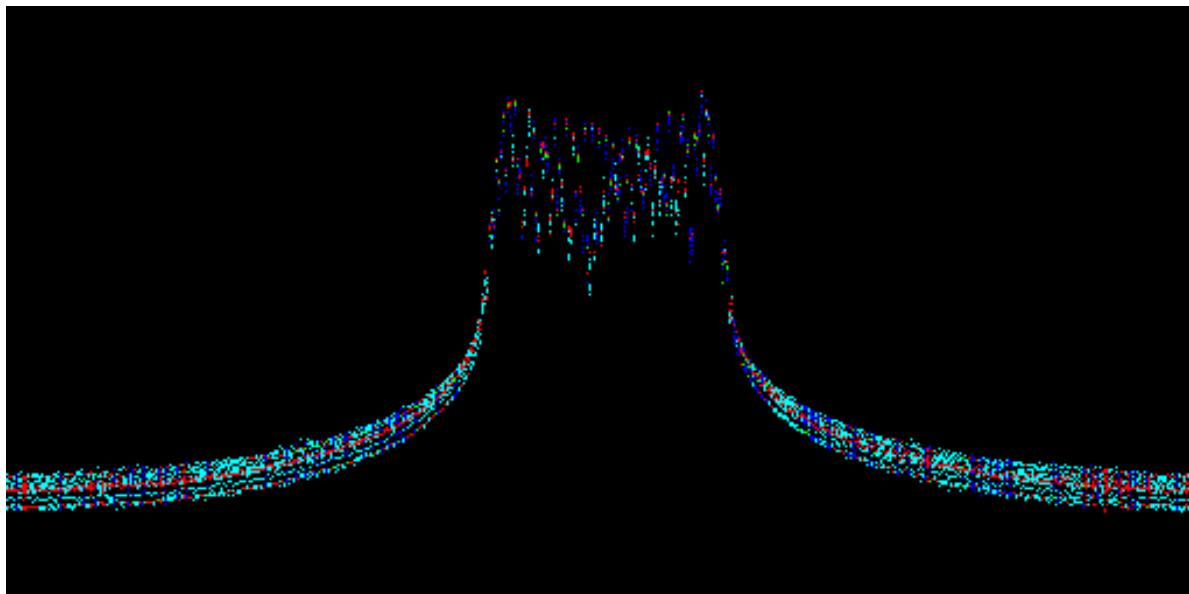
- **Spectrogram**

- overlapping ratio = 0.5, Nb slots = 8
 - display refresh frequency → 5.7Hz
- overlapping ratio = 0.8, Nb slots = 16
 - display refresh frequency → 2.6Hz



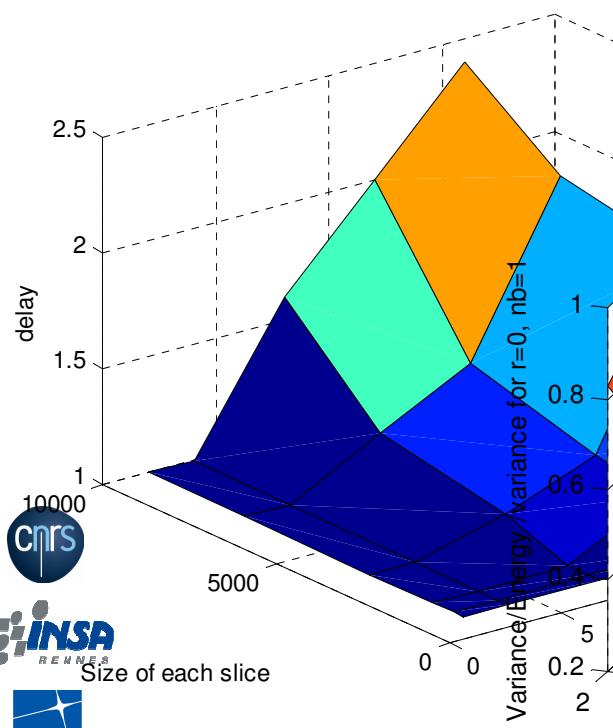
- **Remanence**

- overlapping ratio = 0.5, Nb slots = 8
 - display refresh frequency → **8.33 Hz**
- overlapping ratio = 0.8, Nb slots = 16
 - display refresh frequency → **3.86 Hz**

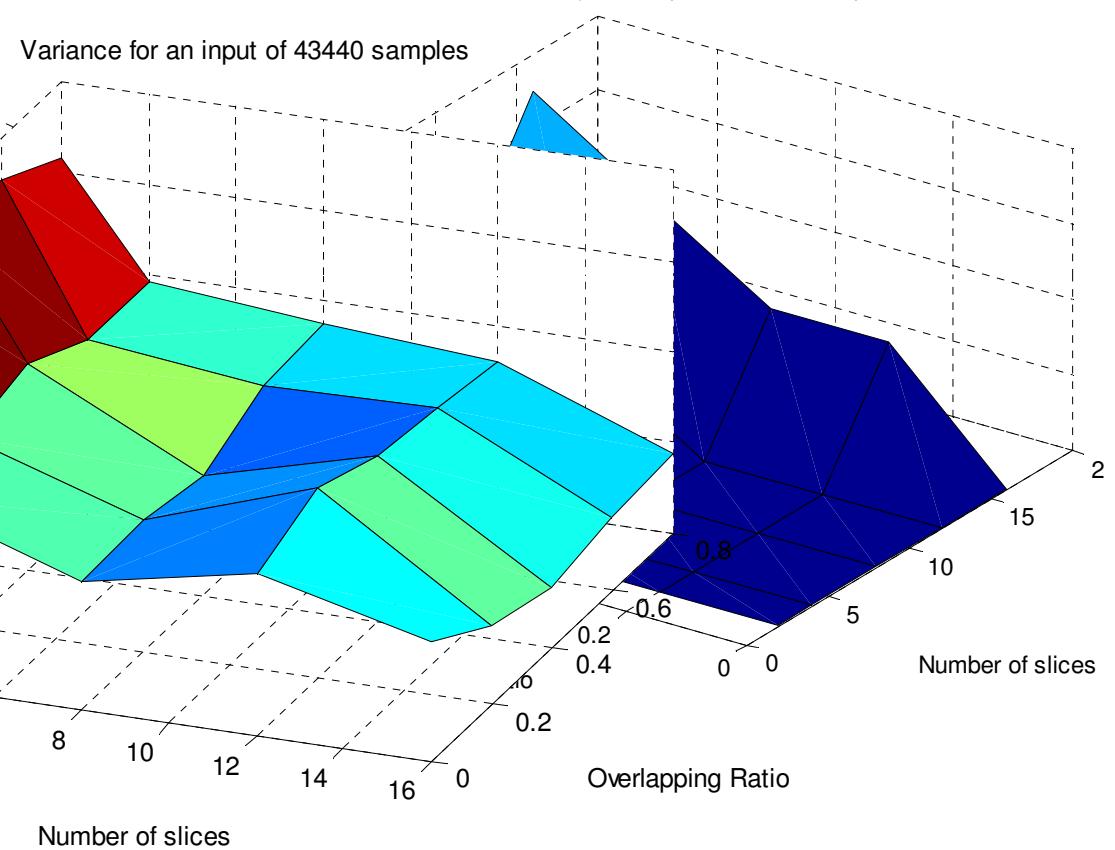


- Trade-off: quality / execution duration

Relative delay for a input of 14480 samples



Relative delay for a input of 14480 samples



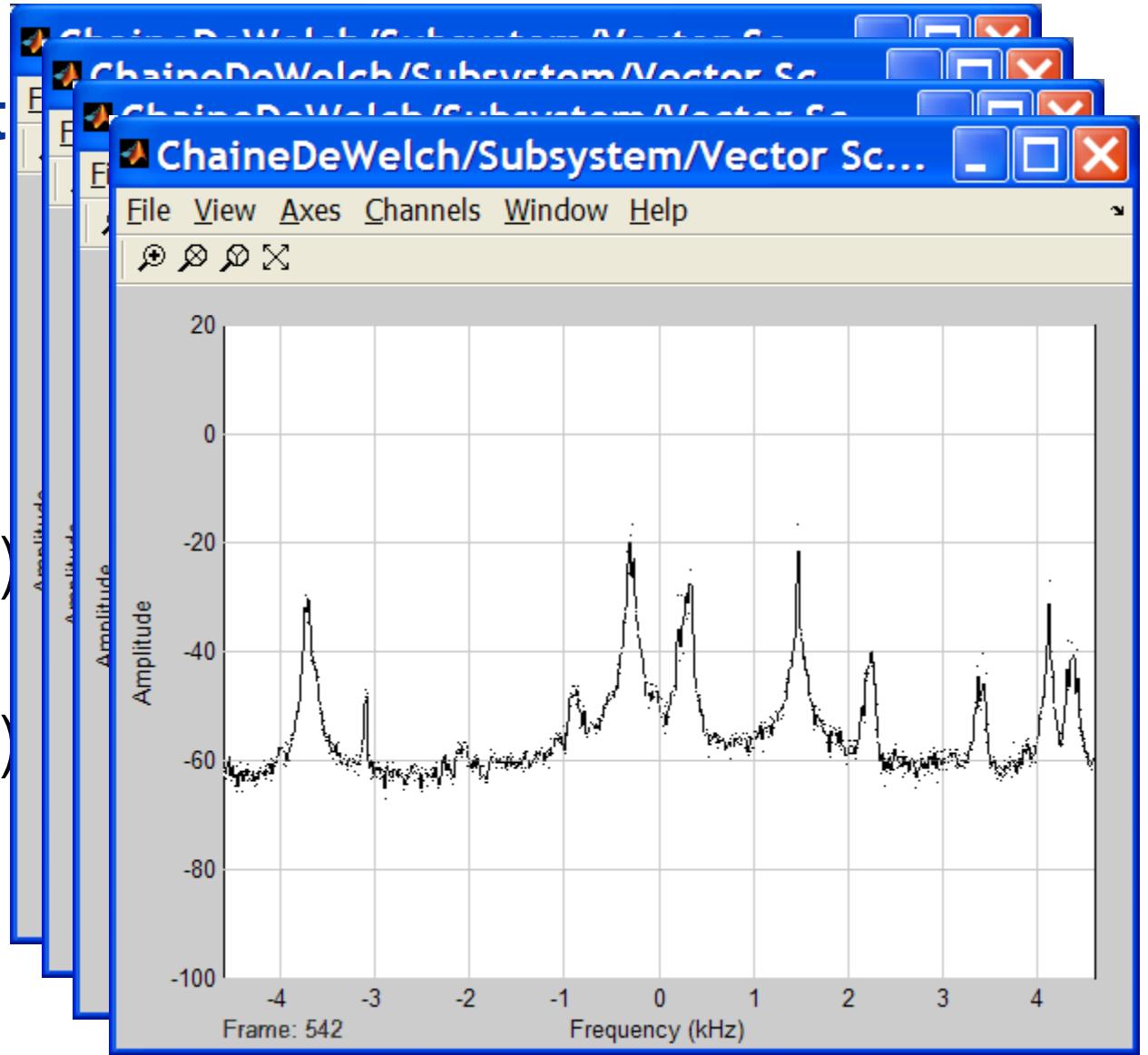
- FM broadcast

- 256

- 512

- 64 (delayed)

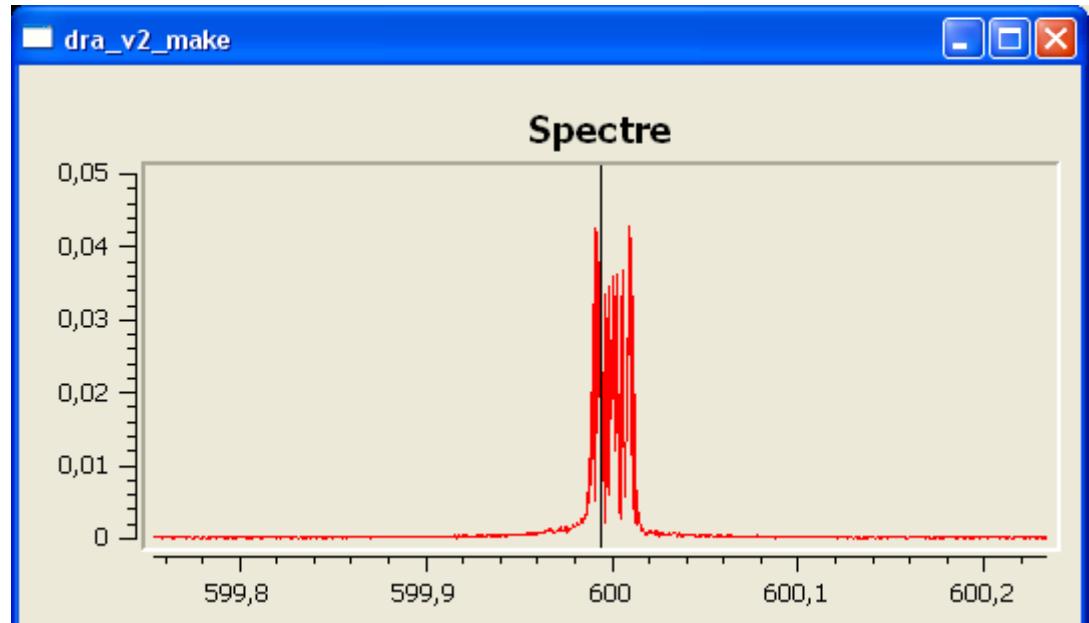
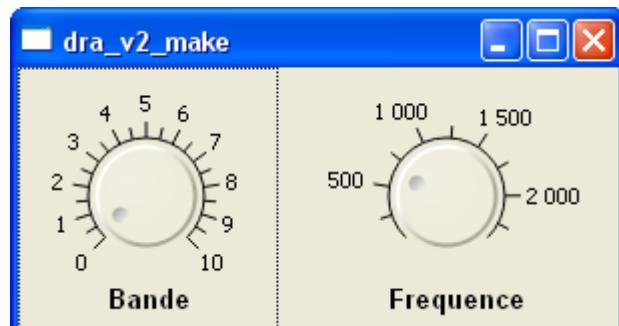
- 16 (delayed)



- Power spectral density
- Simulink implementation on N210 platforms
- Windows implementation on N210 platforms

- **Development Environment**
 - SUPELEC proprietary environment (Windows)
- **UHD library**
 - from Ettus Research
- **Supporting HDCRAM**
 - Hierarchical and Distributed Cognitive Radio Architecture Management [1]
 - HDCRAM is an architecture for the management of reconfiguration and cognitive facilities (metrics capture and decision/learning)
 - for real-time auto-adaptation

- **Spectrum analyzer is a kind of cognitive radio**
 - carrier frequency adjustment → sensor
 - DCR factor (undersampling) → sensor
 - display parameters → data processing to be reconfigured
- **FM 10 kHz**



- Power spectral density
- Simulink implementation on N210 platforms
- Windows implementation on N210 platforms



Conclusion

- **Teaching level (project benefit for students)**
 - power spectrum density study
 - fast and easy to implement
 - very motivating for students compared to
 - analytical analysis
 - simulations more or less disconnected from reality
 - writing a paper for a conference
- **SDR for other stuff than communications**
 - channel sounder last year
 - spectrum analyzer here
- **Sensor for cognitive radio**

- **Thank you to/This is the work of students:**

**Emilien LE SUR
Jérôme PARISOT**

Thanks for your attention



<http://www.rennes.supelec.fr/ren/perso/cmoy/SCEE-SERI/>

