

# IMPLEMENTING A BASE STATION USING THE SDR PLATFORM FOR COEXISTENCE OF HETEROGENEOUS WIRELESS SYSTEMS<sup>1</sup>

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## ABSTRACT

We propose a new approach to solving the spectrum scarcity problem, called spectrum access scheduling (SAS), which allows heterogeneous wireless systems to share the same spectrum band using protocol engineering methods. SAS treats the collection of wireless systems as an ecosystem, and optimizes the system performance by collaborative designs. The key architectural component in realizing SAS is the base stations of the wireless systems. We implement a base station using state-of-the-art software and hardware components, namely GNU Radio, OpenBTS and USRP software and hardware platforms, and demonstrate the feasibility of the coexistence of two heterogeneous wireless systems, GSM and CSMA, in the GSM-900 band.

## 1. INTRODUCTION

According to a recent spectrum usage investigation conducted by the FCC, the RF wireless spectrum is far from fully utilized [1]. According to the report, the typical channel occupancy was less than 15%, and the peak usage was only close to 85%. On the other hand, traffic demands in the wireless networks are growing exponentially over the years, and quickly overwhelm the network capacity of wireless service providers in some parts of the regions, such as hotspots or disaster-stricken areas where limited number of base stations remain. Adaptive and efficient spectrum reuse mechanisms are highly desirable in order to fully utilize the wireless bands.

We propose a spectrum access scheduling (SAS) approach to sharing spectrum among multiple heterogeneous wireless systems. For that purpose, we integrate and implement the base station functions of the wireless systems on the same hardware platform, and require that the wireless systems access the shared medium in a coordinated TDMA fashion so as to avoid channel access collisions. The concurrent execution of multiple wireless systems on the same base station hardware platform allows time-stringent protocol messages to be exchanged in real time.

We implement SAS among heterogeneous wireless systems using the SDR (software defined radio) hardware, because of the programmability and reconfigurability of the software and hardware components of the SDR platform. Specifically, we explore the feasibility of enabling the coexistence of two wireless systems, GSM and CSMA/CA systems, in the GSM-900 spectrum band using the USRP (Universal Software Radio Peripheral) hardware [6] and OpenBTS [4] and GNU Radio [5] software platforms.

In contrast to cognitive radios (CRs) [3] which differentiate wireless spectrum users as primary and cognitive ones, and share the spectrum in an opportunistic manner, our approach using SAS treats all spectrum users as equal share holders of the spectrum, and avoids system operation disruptions using a holistic protocol engineering approach. Overall, the base station hosting the heterogeneous wireless systems appears as if it is multilingual and coordinates the wireless systems to fully utilize the channel.

## 2. DEMO SYSTEM SPECIFICATION

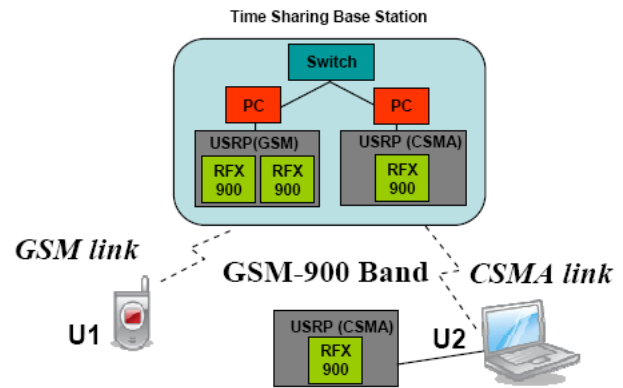


Figure 1 Configuration of base station

Figure 1 illustrates the system architecture for the coexistence of heterogeneous wireless systems in the GSM-900 band using SDR based base station. There are three components in the architecture, namely the base station

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comprised of two sets of one PC and a USRP interconnected by an Ethernet switch, the regular GSM mobile handset, and the mobile computer attached with a USRP. All components operate over the GSM-900 radio frequency band. We specify the details in the following sections.

## 2.1 Spectrum Configuration

	GSM	CSMA
<b>Modulation</b>	GMSK	GMSK
<b>Center Frequency</b>	UL:895.8MHz DL:940.8MHz	895.8MHz
<b>Channel Bandwidth</b>	200KHz	1MHz

Table 1 RF characteristics

We examine SAS mechanisms using smaller settings. As an example in this demonstration, we explore the coexistence mechanisms for GSM and CSMA systems over the GSM-900 RF bands. The RF characteristics of GSM and CSMA are shown in Table 1.

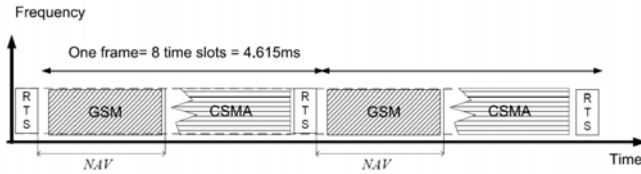


Figure 2 GSM and CSMA time share the GSM-900 band.

The goal of this system is to enable the two wireless systems, GSM and CSMA/CA, to share the 25 MHz wide uplink GSM band at the 895.8 MHz carrier spectrum in a way as shown in Figure 2. In the time domain, GSM and CSMA systems share the GSM-900 band in a round robin fashion. The granularity of the channel division is the GSM frame which lasts for 4.615 milliseconds. Controlled by the base station, three time slots of each GSM time frame are assigned to the GSM system, and the rest of the time slots are assigned to the CSMA system. However, the portions allocated to GSM and CSMA can be flexible, depending on the traffic demands of the wireless systems. In this demonstration, we only discuss the fixed time slot allocations to each of the wireless systems for simplicity.

In order to prevent mobile CSMA/CA stations from accessing the wireless channel inadvertently while the GSM stations are accessing the channel, the base station sends RTS (Request To Send) control frames with NAV (Network Allocation Vector) information to itself so as to reserve the channel time periods for GSM operations [7]. In Figure 2, the NAV period covers the duration in which the GSM system operates. The RTS control frame has been addressed

to the base station itself so that no other stations will reply to the RTS request.

## 2.2 Computing Hardware Configuration

An SAS base station consists of two sets of a general purpose computer (PC), one USRP (Universal Software Radio Peripheral) board for signal sampling, and an RFX 900 boards for RF signal transmission and reception.

For simplicity, we use two sets of computing hardware systems to virtually simulate a single base station that executes the GSM and CSMA systems, respectively. The two computers are synchronized within a few microseconds accuracy using the IEEE 1588 Clock Synchronization standard, so that the individual wireless systems can fine tune their channel access at very high precision. Individually, each of the hardware sets executes the wireless system operations using the existing software components during its allocated time slots. If we were to implement the base station using a single computer, the real-time scheduling function alternating the two wireless system executions for channel access would have been a complicated issue, involving OS kernel programming to satisfy the real-time scheduling requirements. Using two separate computers to implement a virtual base station eliminates the complexity of the OS kernel programming, and allows us to focus on protocol engineering issues for coexistence.

The PCs are configured with Pentium-D Dual Core 2.8GHz processors and 1GB RAM, with USB 2.0 ports for connections with the USRP boards.

The USRP provides a low cost development platform for supporting the SDR RF frontend. A USRP board consists of one motherboard that holds ADC (Analog to Digital Converter) and DAC (Digital to Analog Converters) circuits, and a Field Programmable Gate Array (FPGA). The FPGA implements the control logics to reduce the sampling rate and send the signal samples to the computer over a USB 2.0 connection. The USB 2.0 connection has a theoretic data rate limited to 480 MBit/s. In practice, the data rate of the USB connection is much lower [8].

In order to operate in the GSM-900 bands, both USRPs for the GSM and CSMA systems use daughterboard RFX 900 as the RF frontend for RF signal transmission and reception purposes. RFX 900 modulates the output signal over the carrier frequency and extracts input signals from the carrier. The daughterboard and the USRP motherboard compose a complete RF transceiver system. The daughterboard RFX 900 operates at 750 to 1050 MHz and its transmit power of RFX 900 is 200mW [10][11].

## 2.3 Software Configuration for the GSM Component

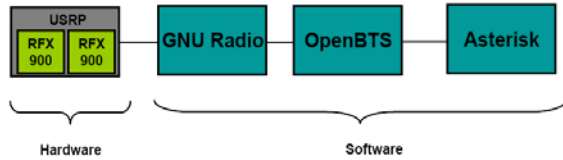


Figure 3 GSM system setting

In this demonstration, the GSM system is implemented over an open-source project, OpenBTS [4]. Figure 3 illustrates the control and data flows of applying the OpenBTS system to support communication through GSM.

The OpenBTS system uses the USRP and RFX 900 daughterboards to receive and transmit GSM signals. The Asterisk is used to control and manage calls for the cellular phones under the OpenBTS network. In OpenBTS, OpenBTS interfaces with `libusrp` libraries to use USRP.

GNU Radio is a free software development toolkit [5]. In GNU Radio, the signal processing runtime and processing blocks are provided to implement software radios using readily-available, low-cost external RF hardware, such as USRP and commodity processors. While performance-critical signal processing blocks are implemented in C++, GNU Radio applications are primarily written by Python, a dynamic object-oriented scripting language. SWIG is used as an interface compiler to allow easy integration of C++ and Python. This way, Python codes simply connect the signal processing blocks and allow them to run at native speed without any interpretation.

Overall, the OpenBTS uses the USRP to present a GSM air interface ("Um") to standard GSM handsets and uses the Asterisk PBX (Private Branch Exchange) software to control and manage calls between GSM handsets and VoIP endpoints. This way, OpenBTS supports GSM functions without supporting functions provided by traditional GSM components – the Base Station Controllers (BSCs), Mobile Switching Centers (MSCs) or Visitors Location Registers (VLRs). Instead, each GSM handset is treated as a Session Initiation Protocol (SIP) endpoint. OpenBTS can be installed and operated at about 1/10 the cost of current GSM technologies, and still be compatible with most of the handsets already on the market [12].

Specifically, when a GSM circuit is established over the air interface, a GSM physical channel is defined by a certain time slot and an Absolute Radio Frequency Channel Number (ARFCN), which includes a pair of physical radio carriers. Furthermore, each physical channel is time-multiplexed into multiple logical channels. The GSM timing is driven by the serving Base Transceiver Station (BTS)

through the Synchronization Channel (SCH) and Frequency Correction Channel (FCCH). The SCH transmits the current value of the TDMA clock. The FCCH generates a tone on the radio channel that is used by the mobile station to discipline its local oscillator.

## 2.4 Software Configuration of the CSMA Component

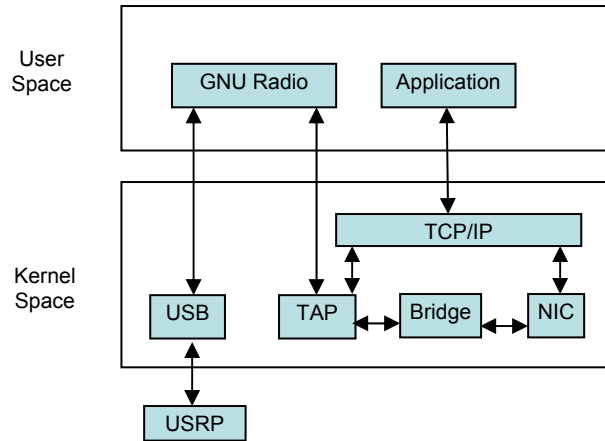


Figure 4 Integration of SDR in the CSMA access point

Figure 4 shows the implementation of the CSMA based wireless system for communication purposes using the USRP and GNU Radio components. One of the GNU Radio examples, `tunnel.py`, has been modified to implement the CSMA system in this demonstration. GNU Radio process runs in the user space, subject to the operating system scheduler.

A TAP [13], a virtual Ethernet network device, is generated in the kernel, so Ethernet frames can be sent and received through the TAP. A virtual bridge is generated to connect the TAP and the physical network interface card (NIC) that connects to the Internet. Application handles the traffic from both TAP and NIC interfaces over the TCP/IP protocol suite.

## 3. DEMONSTRATION RESULTS

As shown in Figure 1, we use existing software and hardware platforms to demonstrate our spectrum access scheduling scheme for the coexistence of heterogeneous wireless systems over the GSM-900 band.

Specifically, we will show that we can carry out the following two communication sessions simultaneously:

- To make a phone call using regular GSM cell phones through the GSM components of the base station configured in this demo;

- To ping a host or browse the Internet using a regular computer through the CSMA components of the base station configured in this demo.

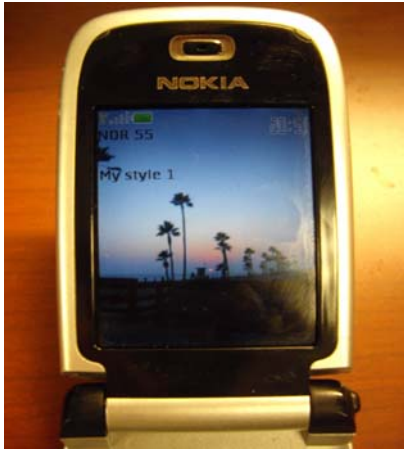


Figure 5 A cell phone under our network (NOR 55).

To demonstrate the communication through GSM system, we show that we can make a phone call through our base station, configured as network NOR55 shown in Figure 5, using our commodity cell phone. The modified GSM-900 system only transmits on the time slot 0, 1, and 2.

To demonstrate the communication through the CSMA system, we carry out a ping operation through the CSMA link using a regular notebook. The IP address of the regular computer is 128.195.55.229. In addition, we can run different applications (such as web browsing, emails etc.) through the CSMA wireless system. Figure 6 shows a screenshot while running the ping function.

In either application, the key point that we demonstrate is the SAS capability that allows heterogeneous wireless systems to share the same RF bands to improve the spectrum utilizations.

#### 4. CONCLUSION

We have demonstrated the spectrum access scheduling (SAS) approach and system implementation details for spectrum reuse purposes. In the demonstrations, we showed that two wireless systems, the GSM and CSMA systems, can share the GSM-900 uplink band in a TDMA fashion. We proved that SAS is not only feasible, but also a promising approach to solving the spectrum scarcity problems.

```

root@dhcp-055223:/usr/local/src/gnuradio_trunk/gnuradio
File Edit View Terminal Tabs Help

root@dhcp-055223:/usr/local/src/... root@dhcp-055223:/usr/local/src/...

PING 128.195.55.244 (128.195.55.244) 56(84) bytes of data.
From 128.195.55.229 icmp_seq=2 Destination Host Unreachable
From 128.195.55.229 icmp_seq=3 Destination Host Unreachable
From 128.195.55.229 icmp_seq=4 Destination Host Unreachable
From 128.195.55.229 icmp_seq=6 Destination Host Unreachable
From 128.195.55.229 icmp_seq=7 Destination Host Unreachable
From 128.195.55.229 icmp_seq=8 Destination Host Unreachable
64 bytes from 128.195.55.244: icmp_seq=12 ttl=64 time=21.8 ms
64 bytes from 128.195.55.244: icmp_seq=13 ttl=64 time=22.7 ms
64 bytes from 128.195.55.244: icmp_seq=14 ttl=64 time=21.2 ms
64 bytes from 128.195.55.244: icmp_seq=17 ttl=64 time=21.8 ms
64 bytes from 128.195.55.244: icmp_seq=18 ttl=64 time=22.9 ms
64 bytes from 128.195.55.244: icmp_seq=19 ttl=64 time=24.0 ms
64 bytes from 128.195.55.244: icmp_seq=28 ttl=64 time=24.0 ms
64 bytes from 128.195.55.244: icmp_seq=29 ttl=64 time=18.6 ms
64 bytes from 128.195.55.244: icmp_seq=35 ttl=64 time=20.4 ms
64 bytes from 128.195.55.244: icmp_seq=39 ttl=64 time=25.6 ms
64 bytes from 128.195.55.244: icmp_seq=40 ttl=64 time=22.1 ms
64 bytes from 128.195.55.244: icmp_seq=42 ttl=64 time=26.2 ms
64 bytes from 128.195.55.244: icmp_seq=54 ttl=64 time=22.4 ms
64 bytes from 128.195.55.244: icmp_seq=55 ttl=64 time=19.8 ms
64 bytes from 128.195.55.244: icmp_seq=60 ttl=64 time=22.3 ms
64 bytes from 128.195.55.244: icmp_seq=64 ttl=64 time=21.8 ms
64 bytes from 128.195.55.244: icmp_seq=65 ttl=64 time=19.7 ms
64 bytes from 128.195.55.244: icmp_seq=71 ttl=64 time=21.7 ms
64 bytes from 128.195.55.244: icmp_seq=74 ttl=64 time=22.5 ms
64 bytes from 128.195.55.244: icmp_seq=75 ttl=64 time=20.2 ms
64 bytes from 128.195.55.244: icmp_seq=76 ttl=64 time=21.2 ms
64 bytes from 128.195.55.244: icmp_seq=81 ttl=64 time=21.1 ms
64 bytes from 128.195.55.244: icmp_seq=82 ttl=64 time=21.3 ms
64 bytes from 128.195.55.244: icmp_seq=86 ttl=64 time=24.5 ms
64 bytes from 128.195.55.244: icmp_seq=92 ttl=64 time=19.3 ms
64 bytes from 128.195.55.244: icmp_seq=96 ttl=64 time=25.3 ms
64 bytes from 128.195.55.244: icmp_seq=101 ttl=64 time=22.3 ms
64 bytes from 128.195.55.244: icmp_seq=102 ttl=64 time=19.8 ms
64 bytes from 128.195.55.244: icmp_seq=111 ttl=64 time=20.7 ms
64 bytes from 128.195.55.244: icmp_seq=115 ttl=64 time=23.6 ms
64 bytes from 128.195.55.244: icmp_seq=116 ttl=64 time=21.1 ms
64 bytes from 128.195.55.244: icmp_seq=117 ttl=64 time=21.9 ms
64 bytes from 128.195.55.244: icmp_seq=121 ttl=64 time=25.4 ms
64 bytes from 128.195.55.244: icmp_seq=126 ttl=64 time=21.5 ms
64 bytes from 128.195.55.244: icmp_seq=131 ttl=64 time=19.8 ms
64 bytes from 128.195.55.244: icmp_seq=132 ttl=64 time=21.7 ms
64 bytes from 128.195.55.244: icmp_seq=135 ttl=64 time=22.6 ms
64 bytes from 128.195.55.244: icmp_seq=136 ttl=64 time=19.8 ms
64 bytes from 128.195.55.244: icmp_seq=137 ttl=64 time=24.9 ms
64 bytes from 128.195.55.244: icmp_seq=139 ttl=64 time=20.7 ms
64 bytes from 128.195.55.244: icmp_seq=143 ttl=64 time=20.6 ms
64 bytes from 128.195.55.244: icmp_seq=158 ttl=64 time=20.5 ms
64 bytes from 128.195.55.244: icmp_seq=159 ttl=64 time=22.4 ms
64 bytes from 128.195.55.244: icmp_seq=162 ttl=64 time=24.1 ms
64 bytes from 128.195.55.244: icmp_seq=167 ttl=64 time=21.3 ms

```

Figure 6 A screenshot while using the ping function

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